SiC MODULE Spec.No.IGBT-SP-21025 R3 P 1

MSM600GS33ALT

SiC MOSFET 3300V

FEATURES

- * Ultra low switching loss with SiC MOSFET
- * High current density package
- * Low stray inductance & low Rth(j-c)
- * Half-bridge (2in1)
- * Built in temperature sensor
- * Scalable large current easily handled by paralleling
- * Equipped with current sensing terminals
- * Sintered copper bonding technology
- * SBD-less SiC module

ABSOLUTE MAXIMUM RATINGS (T_C=25°C)

Item		Symbol	Unit	MSM600GS33ALT
Drain Source Voltage		V _{DSS}	V	3,300
Gate Source Voltage		V _{GSS}	V	+20/-15
Drain Current	DC	I _D	^	600
Drain Current	1ms	I _{DM}	A	1,200
Source Current	DC	Is	Λ	600
Source Current	1ms	I _{SM}	— A	1,200
Junction Temperature	·	T _{vj op}	°C	-50 ~ + 175
Storage Temperature		T _{stg}	°C	-55 ~ + 150
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Corow Torque	Terminals (M3/M8)	M	NI m	0.8/15
Screw Torque	Mounting (M6)	M	— N⋅m	6.0 (1)

Notes: (1) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

lt	em	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Drain Source Cut-Off Current			Л	-	-	0.05	V _{DS} =3,300V, V _{GS} =0V, T _{vi} =25°C
Drain Source Cut-C	on Current	I _{DSS}	mA	-	-	1	V _{DS} =3,300V, V _{GS} =0V, T _{vi} =175°C
Cata Cauraa Laaka	as Current		~ Λ	-	-	+100	V _{GS} =20V, V _{DS} =0V, T _{vi} =25°C
Gate Source Leaka	ige Current	I _{GSS}	nA	-100	-	-	V _{GS} =-15V, V _{DS} =0V, T _{vi} =25°C
Drain Cauras an atr	nto Voltogo	W	V	-	2.3	-	I _D =600A, V _{GS} =15V, T _{Vi} =25°C
Drain Source on-sta	ale vollage	$V_{DS(on)}$	V	-	4.2	5.5	I _D =600A, V _{GS} =15V, T _{vi} =175°C
Gate Source Thres	hold Voltage	V _{GS(th)}	V	2.2	3.0	3.8	V _{DS} =10V, I _D =600mA, T _{vi} =25°C
Input Capacitance		Ciss	nF	-	173	-	V _{DS} =10V, V _{GS} =0V, f=100kHz, T _{vi} =25°C
Internal Gate Resis	tance	R _{G(int)}	Ω	-	2.4	-	VDS=10V, VGS=0V, I=100KHZ, I _{VJ} =25 C
Turn On Delay Time	е	t _{d(on)}		-	1.3	-	V _{DD} =1,800V, I _D =600A
Rise Time		t _r		-	0.4	-	$L_{S}=40$ nH, $R_{G(ON/OFF)}=1.5/2.2\Omega$ (2)
Turn Off Delay Time	е	t _{d(off)}	μS	-	1.5	-	$V_{GS}=+15/-10V$, $T_{vi}=175^{\circ}C$
Fall Time		t _f		-	0.25	-	VGS=+13/-10V, 1 _{Vj} =1/3 C
			V	-	1.7	-	I _S =600A, V _{GS} =15V, T _{vj} =25°C
Source Drain Voltage	20	V _{SD}		-	3.8	5	I _S =600A, V _{GS} =15V, T _{vj} =175°C
Source Diam voitag	y c	VSD		-	8.2	-	I _S =600A, V _{GS} =-10V, T _{vj} =25°C
				-	6.4	-	$I_S=600A$, $V_{GS}=-10V$, $T_{vj}=175$ °C
Reverse Recovery	Time	t _{rr}	μS	_	0.65	-	V _{DD} =1,800V, I _S =600A, L _S =40nH,
	Tillie	**					$R_{G(ON/OFF)}=1.5/2.2\Omega$, $T_{vj}=175^{\circ}C$
Turn On Loss		Eon	J/P	-	0.7	-	$V_{DD}=1,800V, I_{D}=600A,$
Turn Off Loss		E _{off}	J/P	-	0.3	-	$L_{S}=40$ nH, $R_{G(ON/OFF)}=1.5/2.2\Omega$ (2)
Reverse Recovery Loss		Err	J/P	-	0.04	-	V _{GS} =+15V/-10V, T _{vj} =175°C
Stray inductance module		L _{SCE}	nH	-	10	-	Between D1(main) and S2(main)
	Resistance	R ₂₅	kΩ	-	5	-	Tc=25°C
NTC-Thermistor	Deviation	ΔR/R	%	-5	-	5	Tc=25°C
	B-constant	B _(25/50)	K	-	3375	-	Between 25°C and 50°C
Thermal Impedance MOS		R _{th(j-c)}	K/W	-	-	0.033	Junction to case
Contact Thermal Im	npedance	R _{th(c-f)}	K/W	-	0.02	-	Case to fin(par 1 arm)

Notes: (2) R_G value is a test condition value for evaluation, not recommended value.

Please determine the suitable R_{G} value by measuring switching behavior and checking results with the respective SOA.

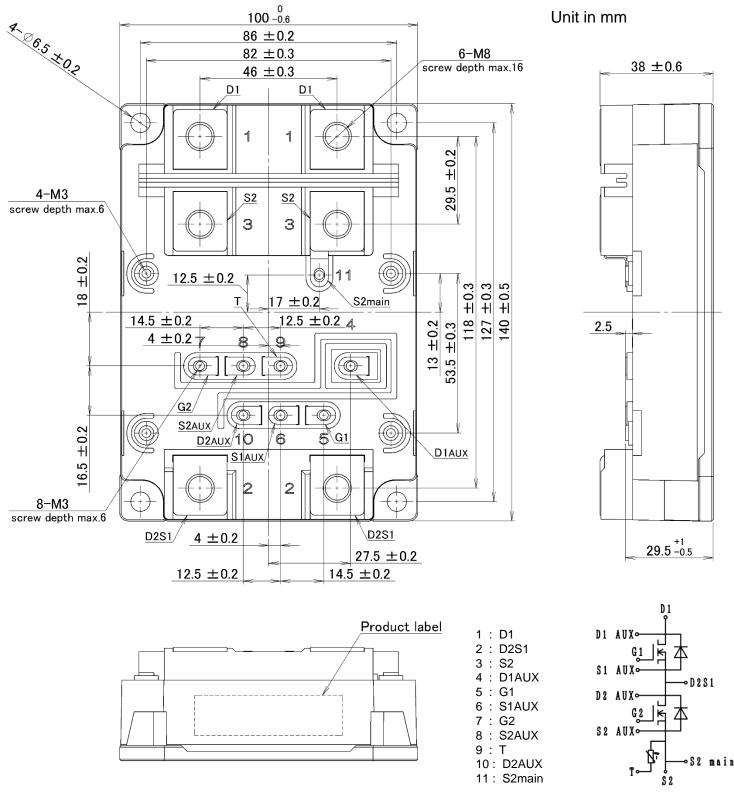
^{*} Please contact our representatives at order.

^{*} For improvement, specifications are subject to change without notice.

^{*} For actual application, please confirm this spec sheet is the newest revision.

^{*} ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

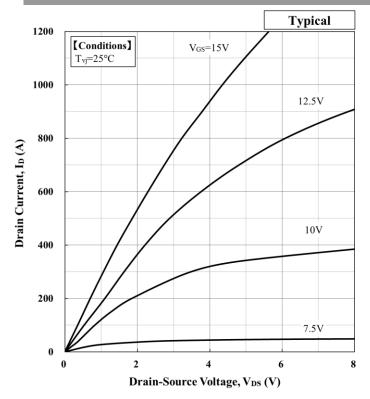
OUTLINE DRAWING



Weight: 770(g)

Terminal Number

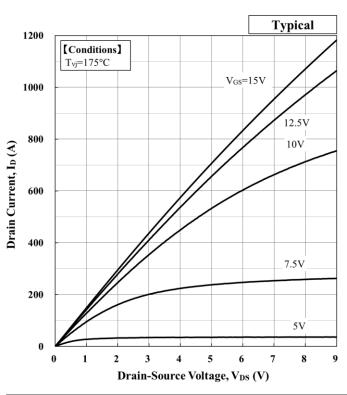
Circuit Diagram



$V_{DS}[V] = a_3 \cdot I_D ^3 + a_2 \cdot I_D ^2 + a_1 \cdot I_D + a_0$							
Temp. [°C]	V _{GS} [V]	a ₃	a_2	a_1	a_0		
25	15	5.89E-10	4.31E-07	3.32E-03	1.75E-02		

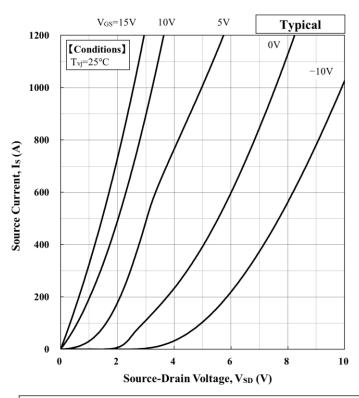
$V_{DS}[V] = a_3 \cdot I_D ^3 + a_2 \cdot I_D ^2 + a_1 \cdot I_D + a_0$							
Temp. [°C] $V_{GS}[V]$ a_3 a_2 a_1 a_0							
150	15	2.72E-10	2.99E-07	5.71E-03	1.61E-02		

Drain Current vs. Drain - Source Voltage

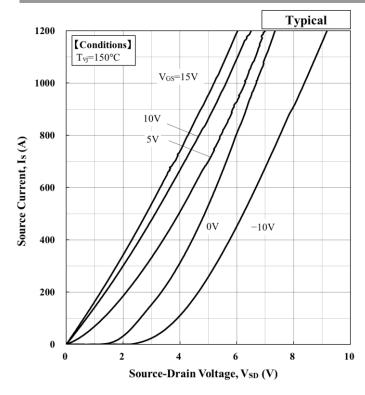


$V_{DS}[V] = a_3 \cdot I_D ^3 + a_2 \cdot I_D ^2 + a_1 \cdot I_D + a_0$						
Temp. [°C]	$V_{GS}[V]$	\mathbf{a}_3	a_2	a_1	a_0	
175	15	4.21E-10	3.41E-07	6.63E-03	5.43E-03	

Drain Current vs. Drain - Source Voltage



$V_{SD}[V] = a_3 \cdot I_S ^3 + a_2 \cdot I_S ^2 + a_1 \cdot I_S + a_0$							
Temp. [°C]	$V_{GS}[V]$	a ₃	a_2	a_1	a_0		
25	15	2.46E-10	-1.09E-06	3.41E-03	1.09E-04		

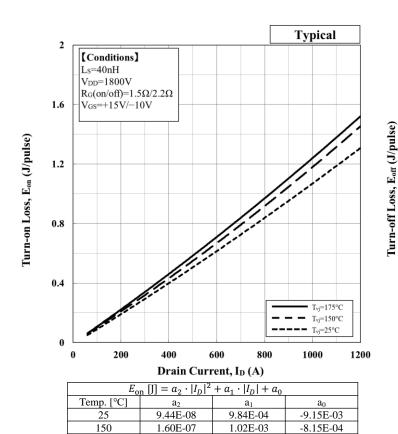


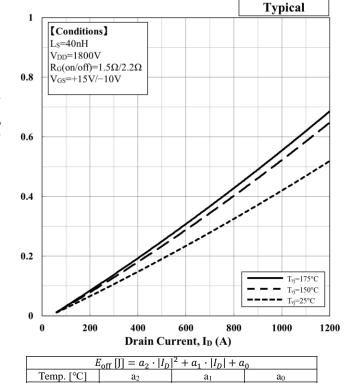
	1200	Typical	
	1200	[Conditions] T _{vj} =175°C	
	1000		
(s (A)	800		
Source Current, Is (A)	600	V _{GS} =15V	
Source	400	10V 5V	
	200	0V /-10V	
	0		10
		Source-Drain Voltage, V _{SD} (V)	

$V_{SD}[V] = a_3 \cdot I_S ^3 + a_2 \cdot I_S ^2 + a_1 \cdot I_S + a_0$						
Temp. [°C]	$V_{GS}[V]$	a ₃	a ₂	a_1	a_0	
150	15	2.62E-10	-1.27E-06	6.19E-03	1.07E-02	

Source Current vs. Source - Drain Voltage

Source Current vs. Source - Drain Voltage





Turn-on loss vs. Drain current

1.11E-03

-4.50E-03

1.33E-07

175

 25
 5.28E-08
 3.79E-04
 -1.23E-02

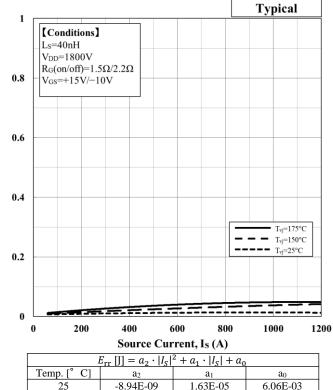
 150
 7.62E-08
 4.63E-04
 -1.66E-02

 175
 7.19E-08
 5.01E-04
 -1.85E-02

Turn-off loss vs. Drain current

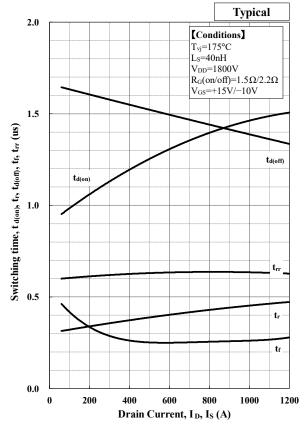
Reverse Recovery Loss, Err (J/pulse)

MSM600GS33ALT

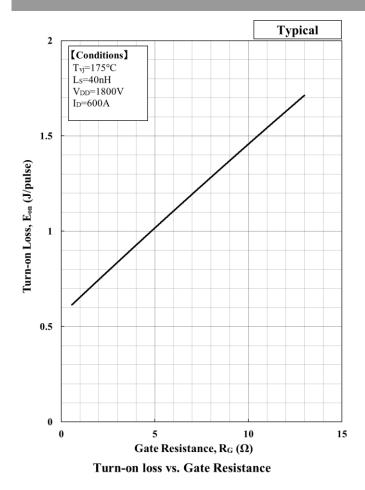


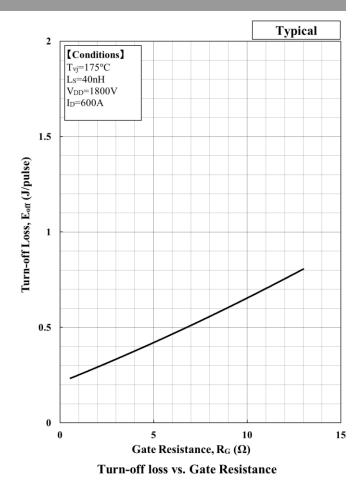
$E_{\rm rr}[J] = a_2 \cdot I_S ^2 + a_1 \cdot I_S + a_0$							
Temp. [° C]	a_2	a_1	a_0				
25	-8.94E-09	1.63E-05	6.06E-03				
150	-6.66E-09	3.66E-05	7.49E-03				
175	-3.28E-08	7.34E-05	7.51E-03				

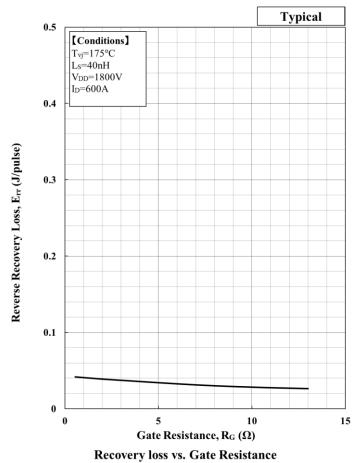
Recovery loss vs. Source current

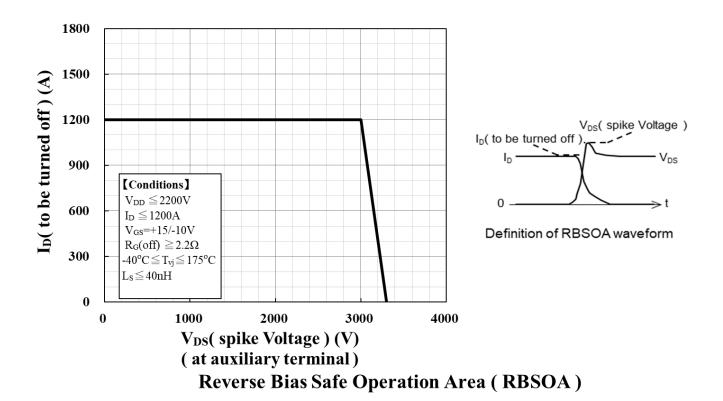


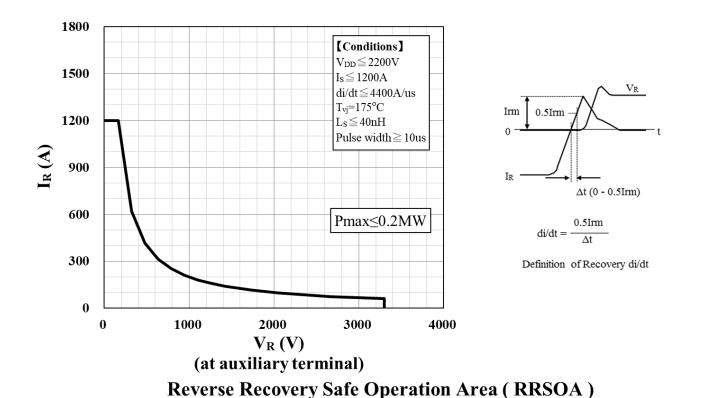
Switching time vs. Drain Current











1.50E-03

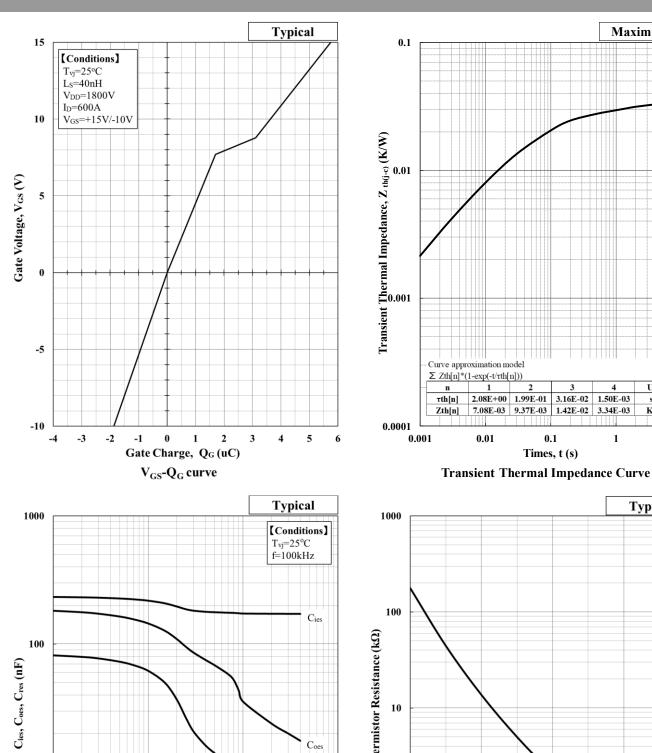
K/W

Typical

10

Maximum

MSM600GS33ALT



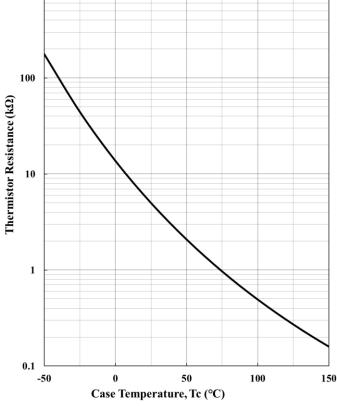
Cres

100

Drain-Source Voltage, VDS (V) Capacitance vs. Drain - Source Voltage

10

0.1



Thermistor Resistance vs. Temperature

Minebea POWER SEMICONDUCTORS

Notices

- 1. Since mishandling of semiconductor devices may cause malfunctions, please be sure to read "Precautions for Safe Use and Notices" in the individual brochure before use.
- 2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
- 3. Semiconductor devices may sometimes break down by accidental or unexpected surge voltage, so please be careful about the safety design such as redundant design and malfunction prevention design which don't cause the damage expand even if they break down.
- 4. In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement. Or consult with MPSD's sales department staff. (When semiconductor devices fail, as a result the semiconductor devices or wiring, wiring pattern may smoke, ignite, or the semiconductor devices themselves may burst.)
- 5. A semi-processed article is done now using solder which contains lead inside the semiconductor devices. There is possibility of the regulation substance depend on the applied models, so please check before using.
- 6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
- 7. The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact with Minebea power semiconductor sales department for the latest version of this data sheets
- 8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).

For inquiries relating to the products, please contact nearest representatives that is located "Inquiry" portion on the top page of a home page.

Minebea POWER SEMICONDUCTORS

■ Usage I

- MPSD warrants that the MPSD products have the specified performance according to the respective specifications at the time of its sale. Testing and other quality control techniques of the MPSD products by MPSD are utilized to the extent MPSD needs to meet the specifications described in this document. Not every device of the MPSD products is specifically tested on all parameters, except those mandated by relevant laws and/or regulations.
- 2. Following any claim regarding the failure of a product to meet the performance described in this document made within one month of product delivery, all the products in relevant lot(s) shall be retested and re-delivered. The MPSD products delivered more than one month before such a claim shall not be counted for such response.
- 3. MPSD assumes no obligation nor makes any promise of compensation for any fault which should be found in a customer's goods incorporating the products in the market. If a product failure occurs for reasons obviously attributable to MPSD and a claim is made within six months of product delivery, MPSD shall offer free replacement or payment of compensation. The maximum compensation shall be the amount paid for the products, and MPSD shall not assume responsibility for any other compensation.
- 4. MPSD reserves the right to make changes in this document and to discontinue mass production of the relevant products without notice. Customers are advised to confirm specification of the product of inquiry before purchasing of the products that the customer desired. Customers are further advised to confirm before purchasing of such above products that the product of inquiry is the latest version and that the relevant product is in mass production status if the purchasing of the products by the customer is suspended for one year or more.
- 5. When you dispose of MPSD products and/or packing materials, comply with the laws and regulations of each country and/or local government. Conduct careful preliminary studies about environmental laws applying to your products such as RoHS, REACH. MPSD shall not assume responsibility for compensation due to contravention of laws and/or regulations.
- 6. MPSD shall not be held liable in any way for damages and infringement of patent rights, copyright or other intellectual property rights arising from or related to the use of the information, products, and circuits in this document.
- 7. No license is granted by this document of any patents, copyright or other intellectual property rights of any third party or of MPSD.
- 8. This document may not be reprinted, reproduced or duplicated, in any form, in whole or in part without the express written permission of MPSD.
- 9. You shall not use the MPSD products (technologies) described in this document and any other products (technologies) manufactured or developed by using them (hereinafter called "END Products") or supply the MPSD products (technologies) and END Products for the purpose of disturbing international peace and safety, including (i) the design, development, production, stockpiling or any use of weapons of mass destruction such as nuclear, chemical or biological weapons or missiles, (ii) the other military activities, or (iii) any use supporting these activities. You shall not sell, export, dispose of, license, rent, transfer, disclose or otherwise provide the MPSD products (technologies) and END Products to any third party whether directly or indirectly with knowledge or reason to know that the third party or any other party will engage in the activities described above.

 When exporting, re-export transshipping or otherwise transferring the MPSD products (technologies)

and END Products, all necessary procedures are to be taken in accordance with Foreign Exchange and Foreign Trade Act (Foreign Exchange Act) of Japan, Export Administration Regulations (EAR) of US, and any other applicable export control laws and regulations promulgated and administered by the governments of the countries asserting jurisdictions over the parties or transaction.