

# MBM450FS33F

Silicon N-channel IGBT 3300V F version

## FEATURES

- \* High current density package
- \* Low stray inductance & low Rth(j-c)
- \* Half-bridge (2in1)
- \* Built in temperature sensor
- \* Scalable large current easily handled by paralleling
- \* Equipped with current sensing terminals

## ABSOLUTE MAXIMUM RATINGS (Tc=25°C )

Item	Symbol	Unit	MBM450FS33F
Collector Emitter Voltage	V <sub>CES</sub>	V	3,300
Gate Emitter Voltage	V <sub>GES</sub>	V	±20
Collector Current	DC	A	450
	1ms		900
Forward Current	DC	A	450
	1ms		900
Operating Junction Temperature	T <sub>vj op</sub>	°C	-50 ~ +150
Storage Temperature	T <sub>stg</sub>	°C	-55 ~ +150
Isolation Voltage	V <sub>ISO</sub>	V <sub>RMS</sub>	6,000(AC 1 minute)
Screw Torque	Terminals (M3/M8)	N·m	0.8/15
	Mounting (M6)		6.0 (1)

Notes: (1) Recommended Value 5.5±0.5N·m

## ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I <sub>CES</sub>	mA	-	-	0.30	V <sub>CE</sub> =3,300V, V <sub>GE</sub> =0V, T <sub>vj</sub> =25°C
			-	15	50	V <sub>CE</sub> =3,300V, V <sub>GE</sub> =0V, T <sub>vj</sub> =150°C
Gate Emitter Leakage Current	I <sub>GES</sub>	nA	-500	-	+500	V <sub>GE</sub> =±20V, V <sub>CE</sub> =0V, T <sub>vj</sub> =25°C
Collector Emitter Saturation Voltage	V <sub>CESat</sub>	V	-	2.25	-	I <sub>C</sub> =450A, V <sub>GE</sub> =15V, T <sub>vj</sub> =25°C
			2.50	3.05	3.50	I <sub>C</sub> =450A, V <sub>GE</sub> =15V, T <sub>vj</sub> =150°C
Gate Emitter Threshold Voltage	V <sub>GE(th)</sub>	V	5.5	6.5	7.5	V <sub>CE</sub> =10V, I <sub>C</sub> =450mA, T <sub>vj</sub> =25°C
Input Capacitance	C <sub>ies</sub>	nF	-	24	-	V <sub>CE</sub> =10V, V <sub>GE</sub> =0V, f=100kHz, T <sub>vj</sub> =25°C
Internal Gate Resistance	R <sub>G(int)</sub>	Ω	-	6.2	-	V <sub>CE</sub> =10V, V <sub>GE</sub> =0V, f=100kHz, T <sub>vj</sub> =25°C
Turn On Delay Time	t <sub>d(on)</sub>	μs	-	0.48	-	V <sub>CC</sub> =1800V, I <sub>C</sub> =450A
Rise Time	t <sub>r</sub>		-	0.12	-	L <sub>s</sub> =40nH
Turn Off Delay Time	t <sub>d(off)</sub>		-	1.10	-	R <sub>G(on/off)</sub> =6.8Ω/12Ω (2)
Fall Time	t <sub>f</sub>		-	1.30	-	V <sub>GE</sub> =±15V, T <sub>vj</sub> =150°C
Forward Voltage Drop	V <sub>F</sub>	V	-	2.25	-	I <sub>F</sub> =450A, V <sub>GE</sub> =0V, T <sub>vj</sub> =25°C
			2.10	2.45	2.80	I <sub>F</sub> =450A, V <sub>GE</sub> =0V, T <sub>vj</sub> =150°C
Reverse Recovery Time	t <sub>rr</sub>	μs	-	1.10	-	V <sub>CC</sub> =1800V, I <sub>F</sub> =450A, L <sub>s</sub> =40nH T <sub>vj</sub> =150°C
Turn-on Loss per Pulse	E <sub>on</sub>	J/P	-	0.73	-	V <sub>CC</sub> =1800V, I <sub>C</sub> =450A, L <sub>s</sub> =40nH
Turn-off Loss per Pulse	E <sub>off</sub>	J/P	-	0.63	-	R <sub>G(on/off)</sub> =6.8Ω/12Ω (2)
Reverse Recovery Loss per Pulse	E <sub>rr</sub>	J/P	-	0.68	-	V <sub>GE</sub> =±15V, T <sub>vj</sub> =150°C
Short Circuit Pulse Width	t <sub>sc</sub>	μs	10	-	-	V <sub>CC</sub> =2200V, L <sub>s</sub> =40nH R <sub>G(on/off)</sub> =6.8/68Ω, V <sub>GE</sub> =±15V, T <sub>vj</sub> =150°C
Stray Inductance Module	L <sub>SCE</sub>	nH	-	9	-	Between C1(main) and E2(main)
NTC-Thermistor	Resistance	R <sub>25</sub>	kΩ	5	-	T <sub>C</sub> =25°C
	Deviation	ΔR/R	%	-5	5	T <sub>C</sub> =25°C
	B-constant	B(25/50)	K	-	3375	Between 25°C and 50°C
Thermal Impedance	IGBT	Rth(j-c)	K/W	-	0.035	Junction to case
	FWD	Rth(j-c)	K/W	-	0.055	
Contact Thermal Impedance	Rth(c-f)	K/W	-	0.02	-	Case to fin (per 1 arm)

Notes: (2) R<sub>G</sub> value is a test condition value for evaluation, not recommended value.Please determine the suitable R<sub>G</sub> value by measuring switching behavior and checking results with the respective SOA.

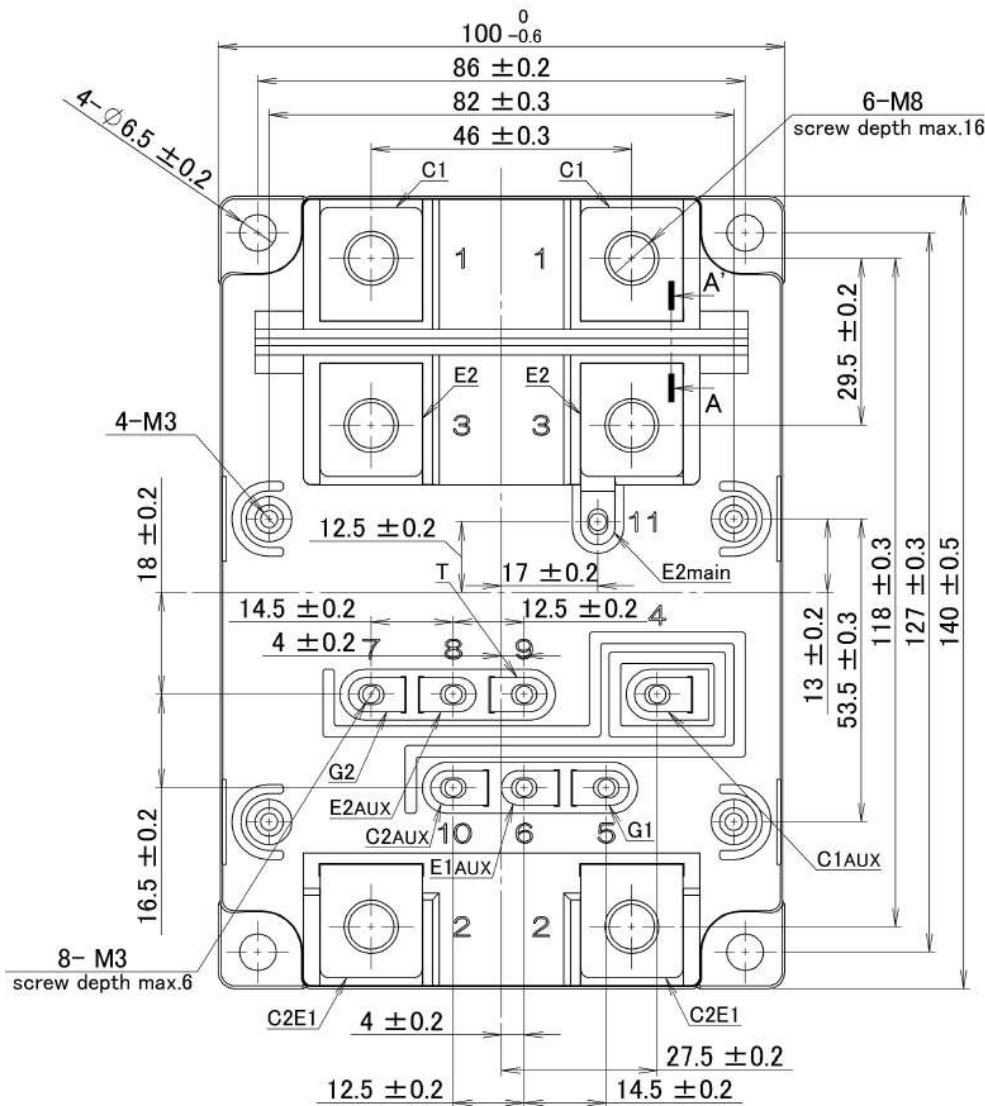
\* Please contact our representatives at order. \* For improvement, specifications are subject to change without notice.

\* For actual application, please confirm this spec sheet is the newest revision.

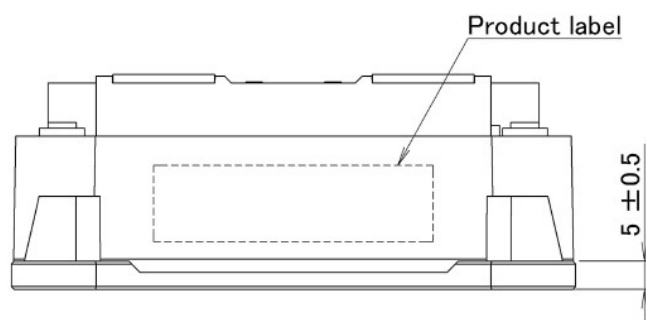
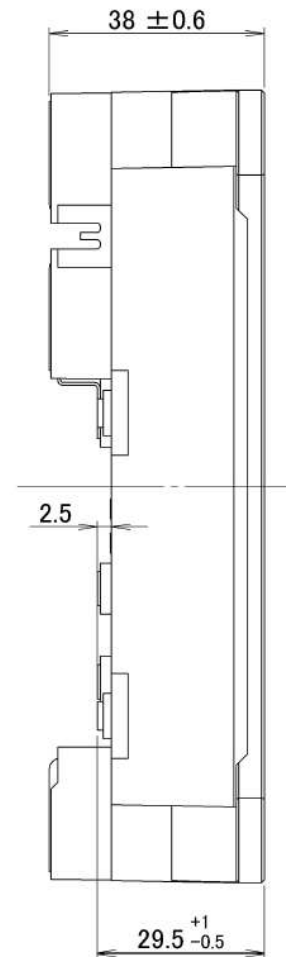
\* ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

# MBM450FS33F

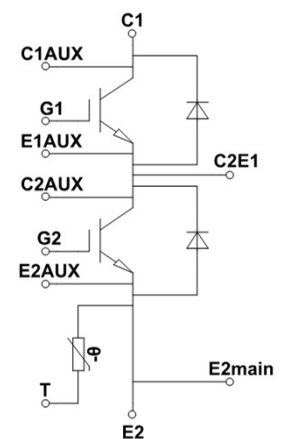
## OUTLINE DRAWING



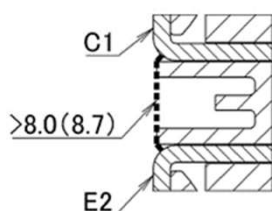
Unit in mm



- 1 : C1
- 2 : C2E1
- 3 : E2
- 4 : C1AUX
- 5 : G1
- 6 : E1AUX
- 7 : G2
- 8 : E2AUX
- 9 : T
- 10 : C2AUX
- 11 : E2main



Clearance between  
C1 and E2 terminal



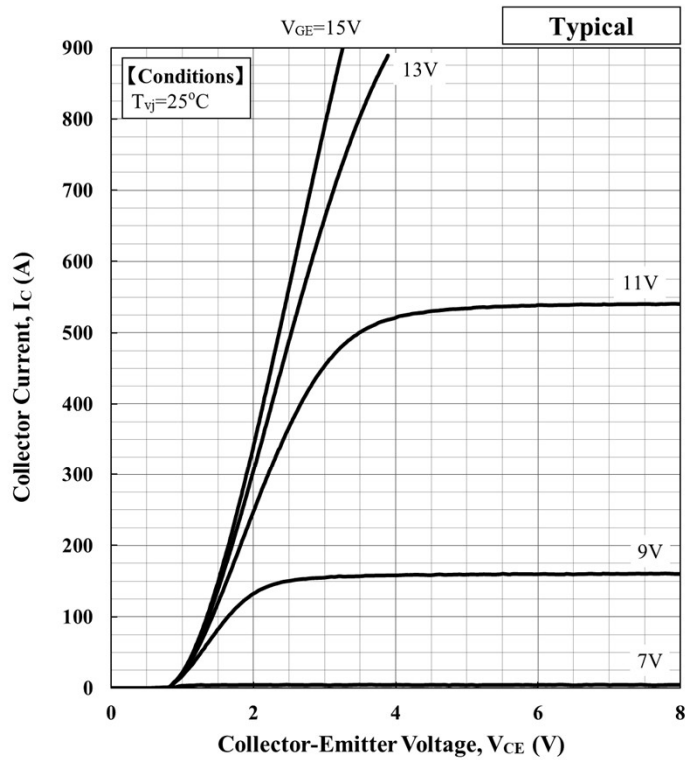
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Weight: 770g

Terminal Number

Circuit Diagram

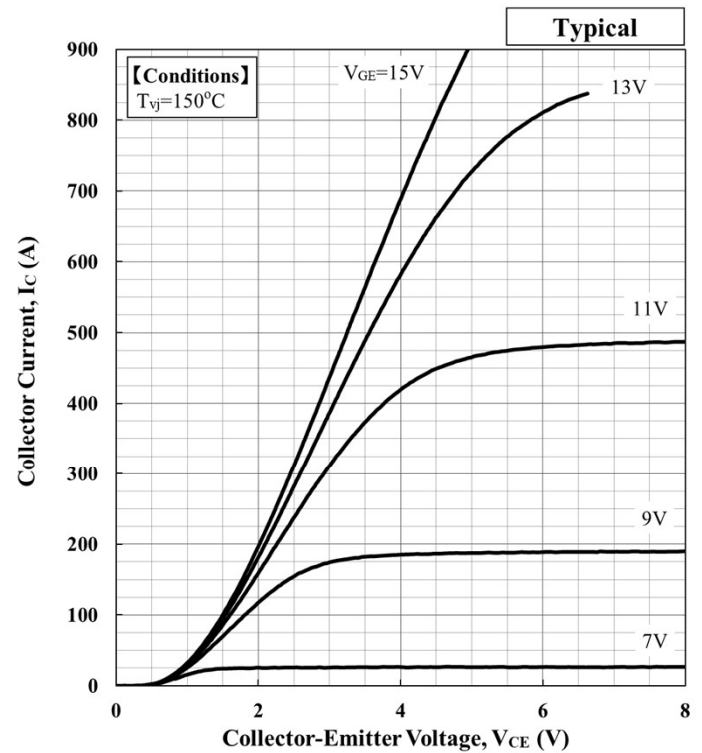
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$$V_{CE(sat)}[V] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	$V_{GE}[V]$	$a_3$	$a_2$	$a_1$	$a_0$
25	15	1.46.E-09	-2.59.E-06	3.67.E-03	9.97.E-01

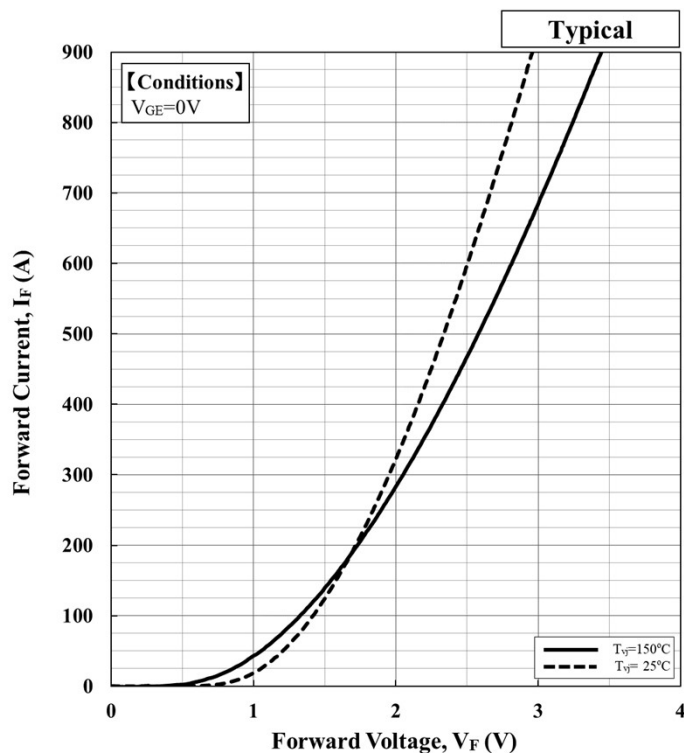
Collector Current vs. Collector Emitter Voltage



$$V_{CE(sat)}[V] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	$V_{GE}[V]$	$a_3$	$a_2$	$a_1$	$a_0$
150	15	3.09.E-09	-4.84.E-06	6.35.E-03	9.01.E-01

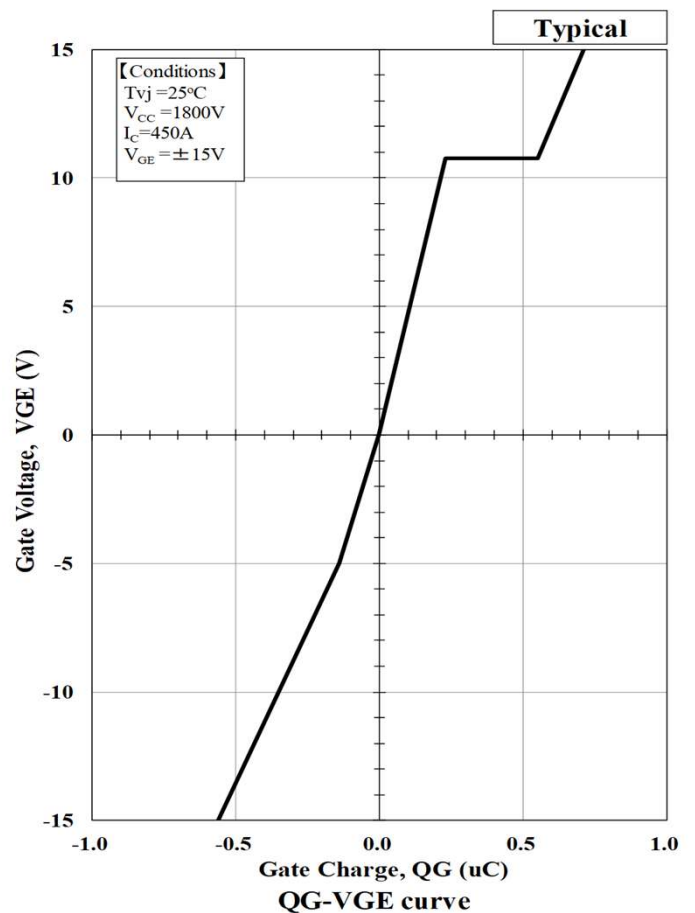
Collector Current vs. Collector Emitter Voltage



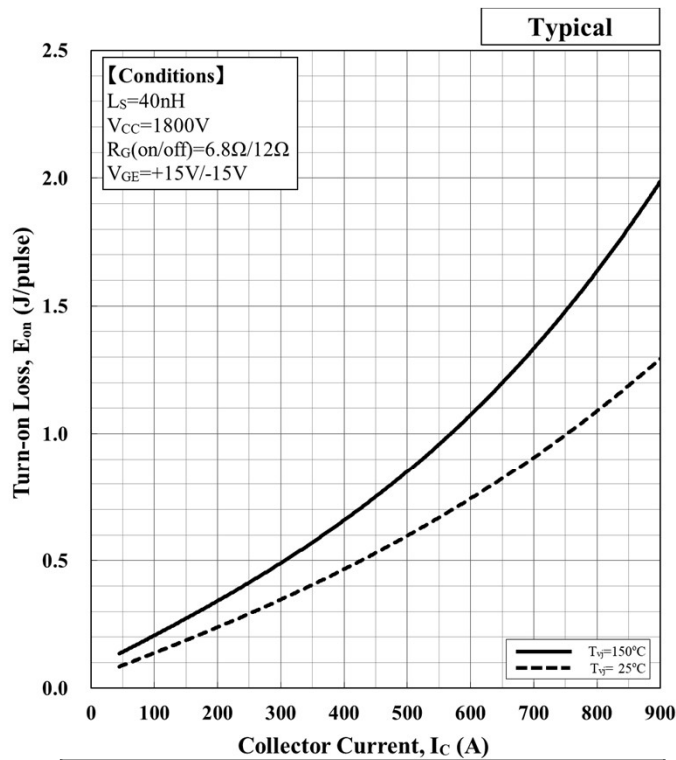
$$V_F[V] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$$

Temp.[°C]	$a_3$	$a_2$	$a_1$	$a_0$
25	1.63.E-09	-3.42.E-06	3.88.E-03	1.06.E+00
150	2.02.E-09	-4.29.E-06	5.12.E-03	8.45.E-01

Forward Voltage of free-wheeling diode

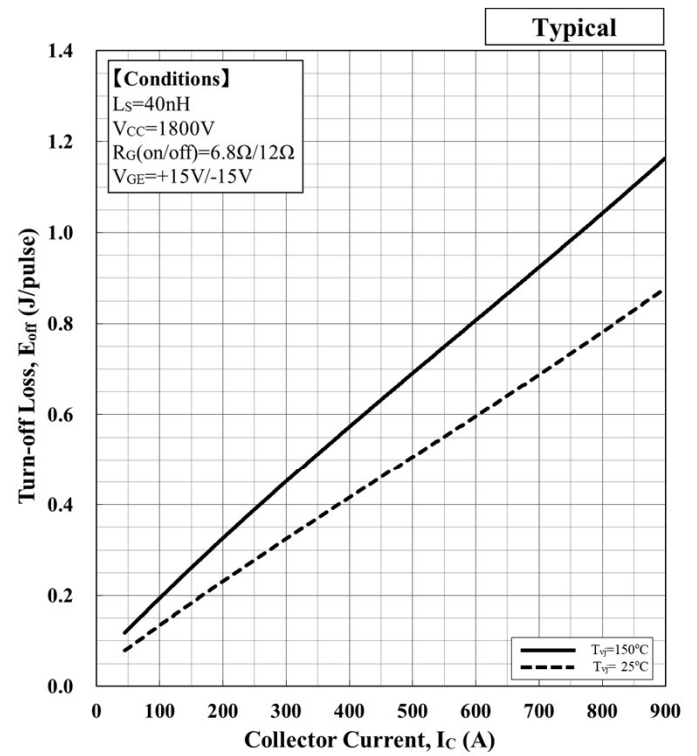


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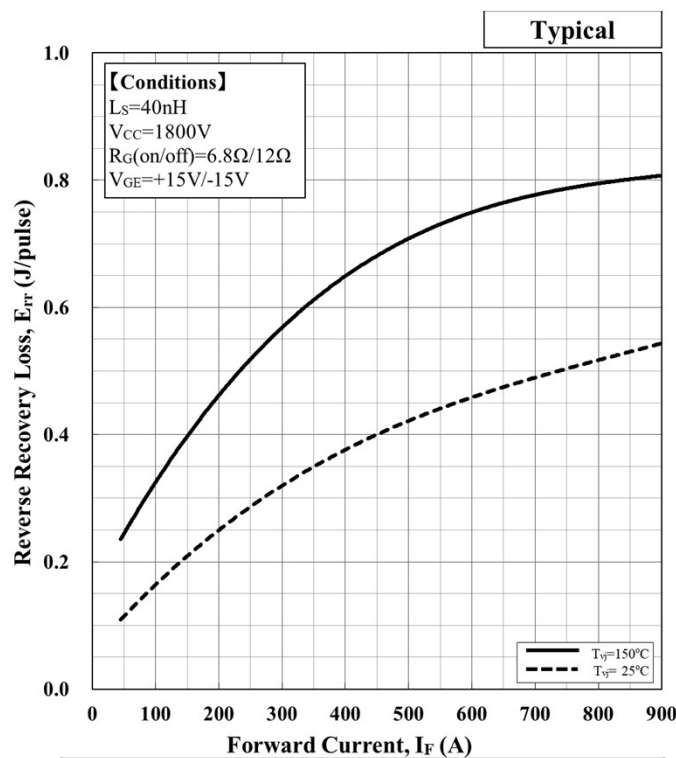
$E [J] = a_3 \cdot  I_c ^3 + a_2 \cdot  I_c ^2 + a_1 \cdot  I_c  + a_0$				
Temp.[°C]	$a_3$	$a_2$	$a_1$	$a_0$
25	3.91E-10	1.55E-07	9.34E-04	4.30E-02
150	9.74E-10	7.79E-08	1.26E-03	7.93E-02

Turn-on loss vs. Collector current



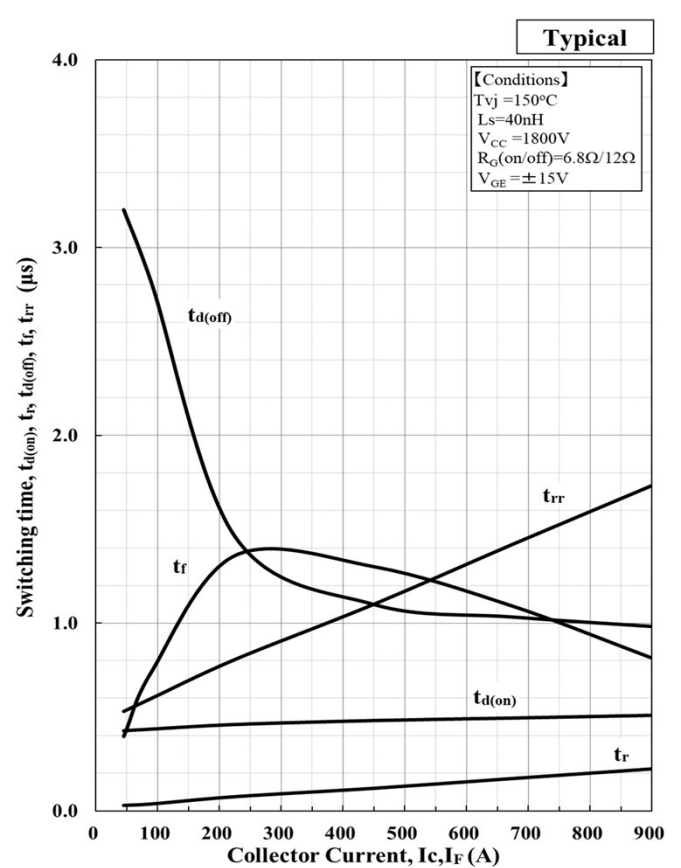
$E [J] = a_3 \cdot  I_c ^3 + a_2 \cdot  I_c ^2 + a_1 \cdot  I_c  + a_0$				
Temp.[°C]	$a_3$	$a_2$	$a_1$	$a_0$
25	1.97E-10	-2.94E-07	1.05E-03	3.25E-02
150	2.71E-10	-4.81E-07	1.45E-03	5.39E-02

Turn-off loss vs. Collector current



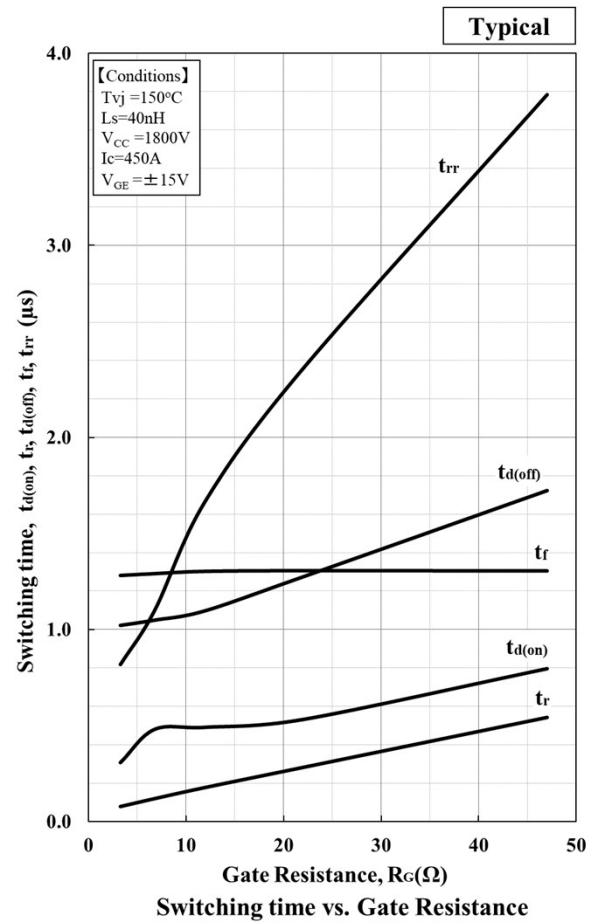
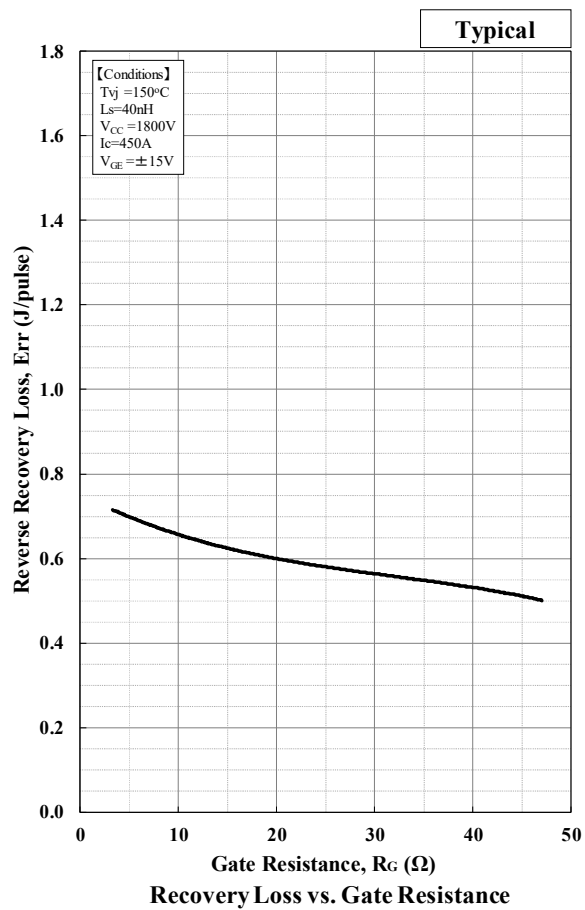
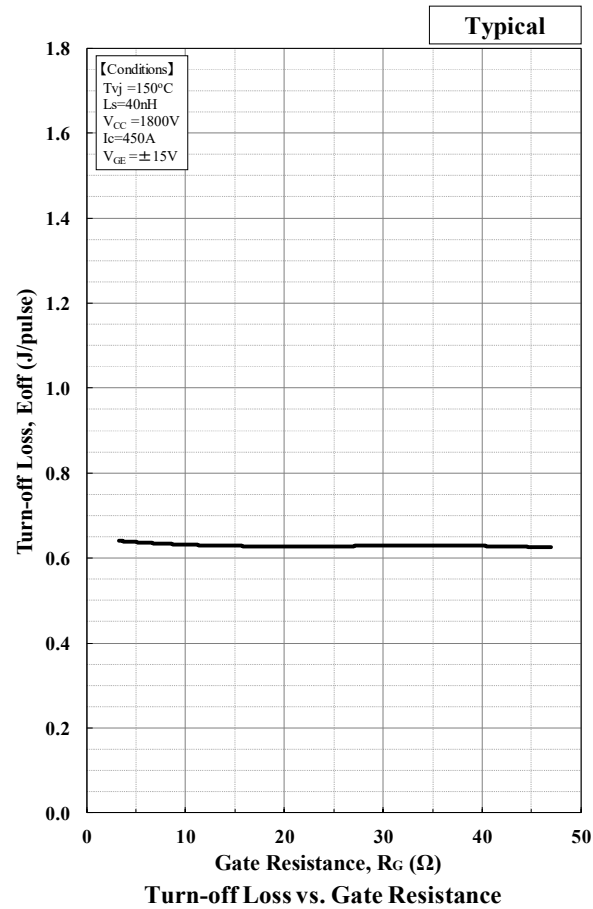
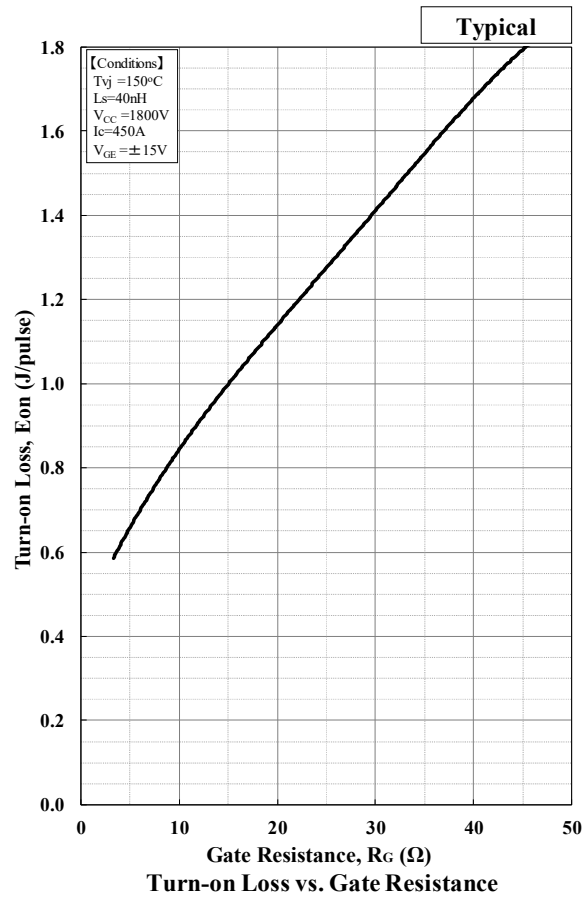
$E [J] = a_3 \cdot  I_F ^3 + a_2 \cdot  I_F ^2 + a_1 \cdot  I_F  + a_0$				
Temp.[°C]	$a_3$	$a_2$	$a_1$	$a_0$
25	4.15E-10	-1.05E-06	1.14E-03	5.99E-02
150	6.76E-10	-1.90E-06	1.89E-03	1.55E-01

Recovery loss vs. Forward current

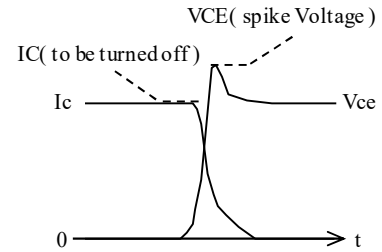
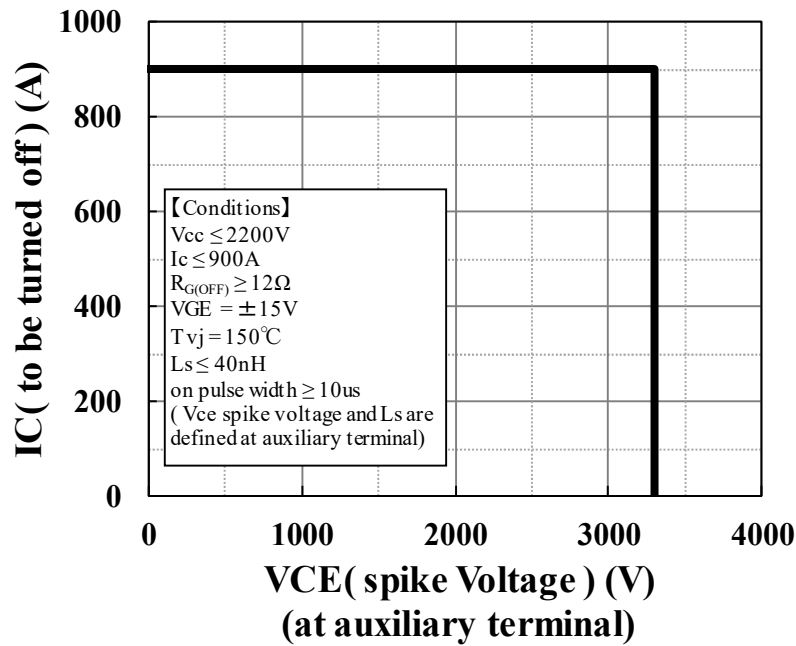


Switching time vs. Collector Current

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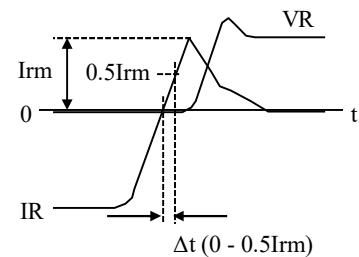
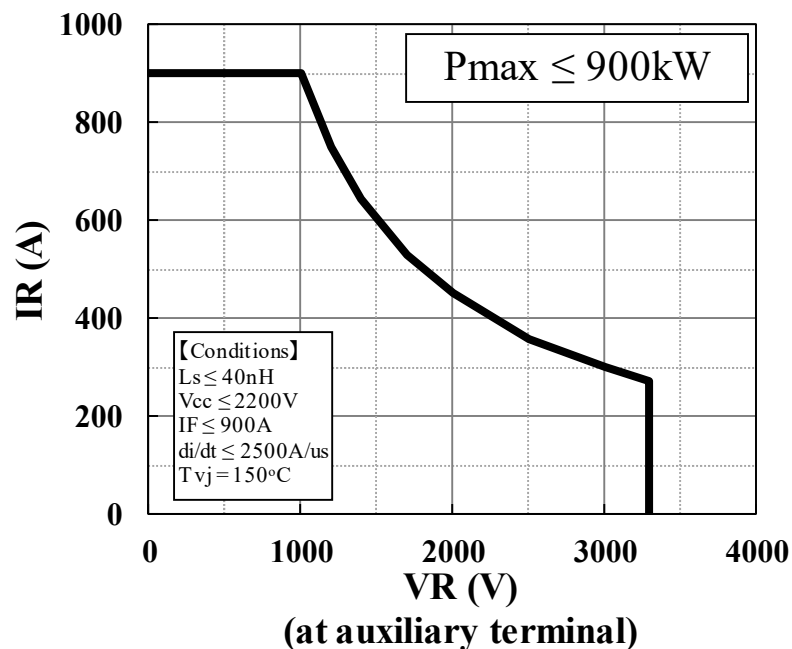


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Definition of RBSOA waveform

## Reverse bias safe operation area ( RBSOA )

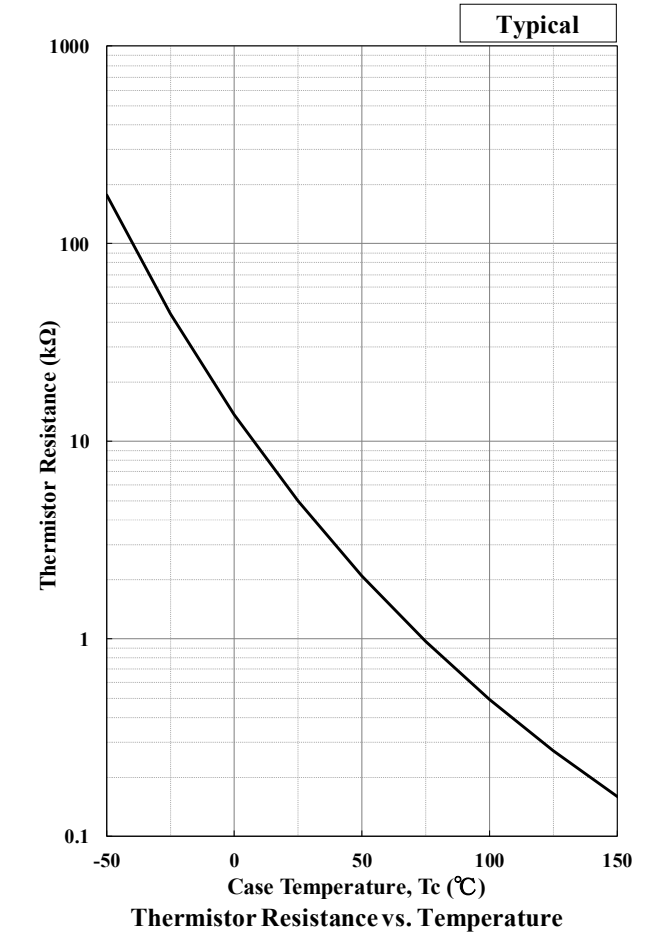
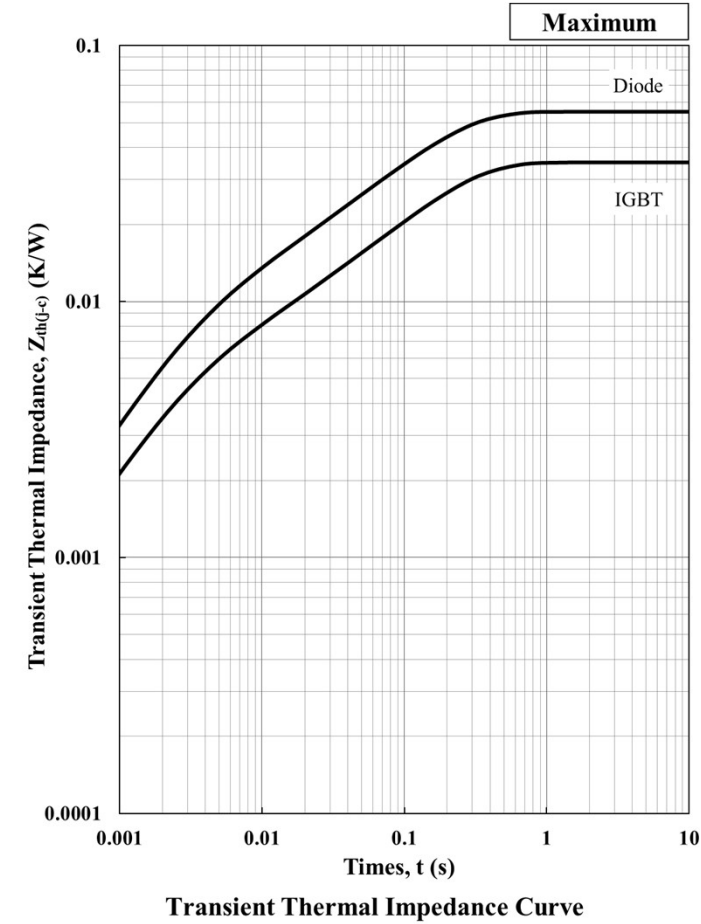
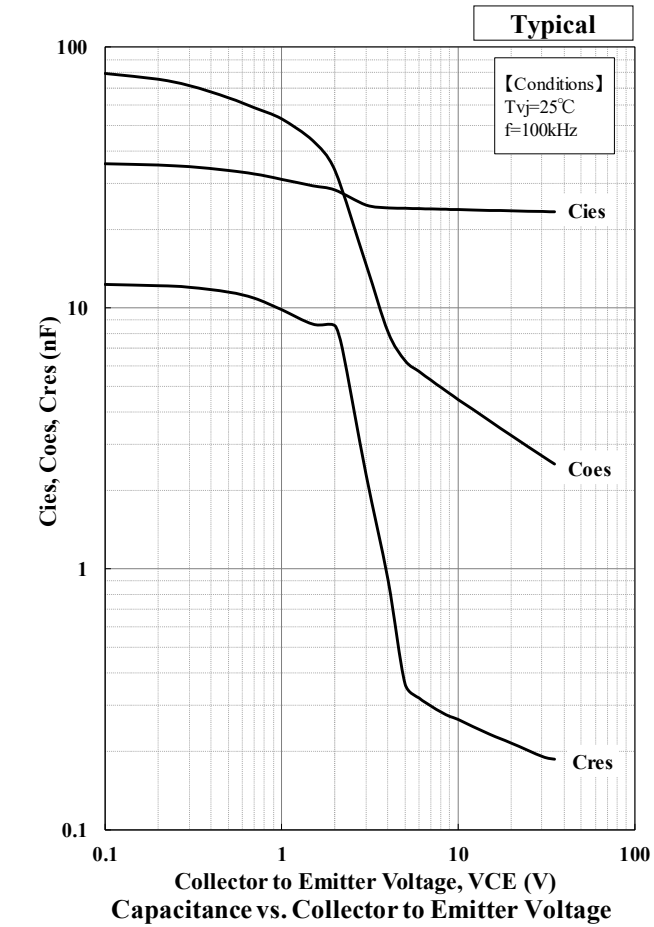


$$\frac{di}{dt} = \frac{0.5I_{rm}}{\Delta t}$$

Definition of Recovery di/dt

## Reverse Recovery SOA

# MBM450FS33F



Foster model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	2.52E-02	4.70E-03	7.66E-04	4.36E-03	[K/W]
C th, IGBT [n]	7.14E+00	4.33E+00	9.74E-01	7.01E-01	[J/K]
R th, Diode [n]	3.70E-02	1.02E-02	9.42E-04	6.76E-03	[K/W]
C th, Diode [n]	4.85E+00	1.99E+00	7.92E-01	4.52E-01	[J/K]

Cauer model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	4.30E-03	3.80E-03	9.63E-03	1.73E-02	[K/W]
C th, IGBT [n]	3.54E-01	5.54E-01	2.57E+00	6.11E+00	[J/K]
R th, Diode [n]	7.17E-03	6.29E-03	1.54E-02	2.62E-02	[K/W]
C th, Diode [n]	2.39E-01	3.15E-01	1.33E+00	4.61E+00	[J/K]

# MBM450FS33F

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1. Since mishandling of semiconductor devices may cause malfunctions, please be sure to read "Precautions for Safe Use and Notices" in the individual brochure before use.
2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
3. Semiconductor devices may sometimes break down by accidental or unexpected surge voltage, so please be careful about the safety design such as redundant design and malfunction prevention design which don't cause the damage expand even if they break down.
4. In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement. Or consult with MPSD's sales department staff. (When semiconductor devices fail, as a result the semiconductor devices or wiring, wiring pattern may smoke, ignite, or the semiconductor devices themselves may burst.)
5. A semi-processed article is done now using solder which contains lead inside the semiconductor devices. There is possibility of the regulation substance depend on the applied models, so please check before using.
6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
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8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).
9. In this module, the maximum depth of the screw holes on the main terminals is 16mm. Using screws longer than 16mm will break the case.

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