Silicon N-channel IGBT 3300V E2 version

FEATURES

* Soft switching behavior & low conduction loss:

Soft low-injection punch-through High conductivity IGBT.

- * Low driving power due to low input capacitance MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High thermal fatigue durability:

(delta Tc=70K, N>30,000cycles)

AISiC base-plate/AIN substrate

ABSOLUTE MAXIMUM RATINGS (T_C=25°C)

Item		Symbol	Unit	MBN1000E33E2
Collector Emitter Voltage		V _{CES}	V	3,300
Gate Emitter Voltage		V _{GES}	V	±20
DC:		Ic	^	1,000
Collector Current	1ms	I _{CRM}	— A	2,000
Forward Current	DC	I _F	^	1,000
Forward Current	1ms	I _{FRM}	— A	2,000
Operating Junction Tempe	erature	T _{vj op}	°C	-40 ~ +150
Storage Temperature		T _{stg}	∘C	-50 ~ +125
Isolation Voltage		V _{ISO}	V _{RMS}	6,000(AC 1 minute)
Corour Torque	Terminals (M4/M8)	-	N·m	2/15 (1)
Screw Torque	Mounting (M6)	-	IN-III	6 (2)

Notes: (1) Recommended Value 1.8±0.2/15⁺⁰-3N·m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I _{CES}	mΑ	-	-	8	$V_{CE}=3,300V, V_{GE}=0V, T_{vj}=25^{\circ}C$
Collector Ethiliter Cut-On Current	ICES	ША	-	14	40	$V_{CE}=3,300V, V_{GE}=0V, T_{vj}=125^{\circ}C$
Gate Emitter Leakage Current	I _{GES}	nA	-500	-	+500	$V_{GE}=\pm20V$, $V_{CE}=0V$, $T_{vj}=25$ °C
Collector Emitter Saturation Voltage	V _{CEsat}	V	2.5	2.95	3.5	I _C =1,000A, V _{GE} =15V, T _{vj} =125°C
Collector Ethitter Saturation Voltage	V CEsat	<u>-</u>	-	3.1	-	I _C =1,000A, V _{GE} =15V, T _{vj} =150°C
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	5.5	6.5	7.5	V _{CE} =10V, I _C =1,000mA, T _{vj} =25°C
Input Capacitance	Cies	nF	-	130	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^{\circ}C$
Internal Gate Resistance	R _{G(int)}	Ω	-	1.5	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, T _{vj} =25°C
Turn On Delay Time	t _{d(on)}		-	0.9	-	V _{CC} =1,650V, I _C =1,000A
Rise Time	t _r	μS	1.6	2.1	2.6	L _S =120nH
Turn Off Delay Time	t _{d(off)}	μδ	-	2.1	-	$R_{G}=3.9\Omega/3.9\Omega$, $C_{GE}=100nF$ (3)
Fall Time	t _f		1.0	1.8	2.7	$V_{GE}=\pm 15V, T_{vj}=125^{\circ}C$
Forward Voltage Drop	VF	V	2.2	2.5	3.0	I _F =1,000A, V _{GE} =0V, T _{vj} =125°C
Forward Voltage Drop	V F	v	-	2.5	-	$I_F=1,000A, V_{GE}=0V, T_{vj}=150^{\circ}C$
Reverse Recovery Time	t _{rr}	μS	0.2	0.8	1.2	V _{CC} =1,650V, I _F =1,000A, L _S =120nH
Reverse Recovery Time	· rr		0.2	0.0	1.2	$T_{vj}=125^{\circ}C$, $R_{G}=3.9\Omega/3.9\Omega$, $C_{GE}=100nF$ (3)
Short Circuit Pulse Width	t _{sc}	0	10	_	_	V _{CC} =2,000V,Ls=130nH
Short Circuit i dise vilatii		μS	10			$R_G(\text{on/off}) = 3.9/39\Omega, V_{GE} = \pm 15V, T_{vj} = 125^{\circ}C$
	E _{on(10%)}		-	2.0	2.4	T _{vi} =125°C
Turn On Loss		J/P	-	2.2	-	·
	E _{on(full)}		-	2.4	-	T _{vj} =150°C V _{CC} =1,650V, I _C =1,000A
	E _{off(10%)}		-	1.4	1.8	T_{vi} =125°C L_s =120nH, R_G =3.9 Ω /3.9 Ω ,
Turn Off Loss		J/P	-	1.5	-	0 100=5 (2)
	E _{off(full)}		-	1.6	-	$T_{vj}=150^{\circ}C$ $V_{GE}=100$ $V_{GE}=15$ $V_{GE}=15$
	E _{rr(10%)}		-	1.0	1.3	T _{vj} =125°C
Reverse Recovery Loss		J/P	-	1.2	-	·
	E _{rr(full)}		-	1.4	-	T _{vj} =150°C
Stray inductance module	L _{SCE}	nΗ	-	18	-	

Notes: (3) R_G and C_{GE} value are a test condition value for evaluation, not recommended value. Please, determine the suitable R_G value by measuring switching behaviors.

- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.

THERMAL CHARACTERISTICS

Item		Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Thermal Impedance	IGBT	R _{th(j-c)}	K/W	-	-	0.012	lunction to coop
Thermal Impedance	FWD	R _{th(j-c)}	r\/vv	-	-	0.024	Junction to case
Contact Thermal Impeda	ince	R _{th(c-f)}	K/W	- 0.007 - Case to fin		Case to fin	

DEFINITION OF TEST CIRCUIT

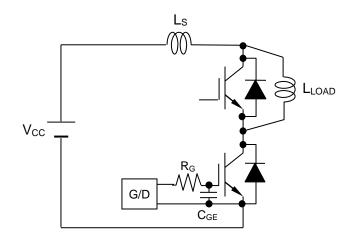


Fig.1 Switching test circuit

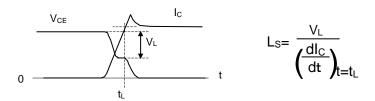


Fig.2 Definition of stray inductance

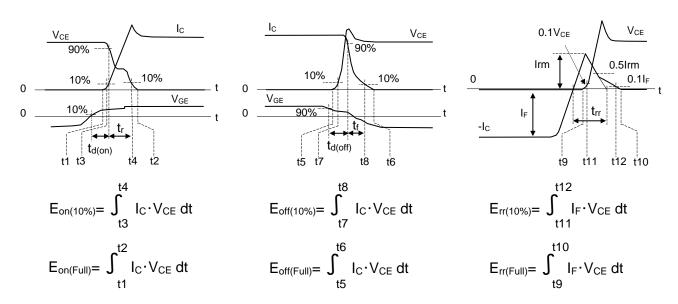
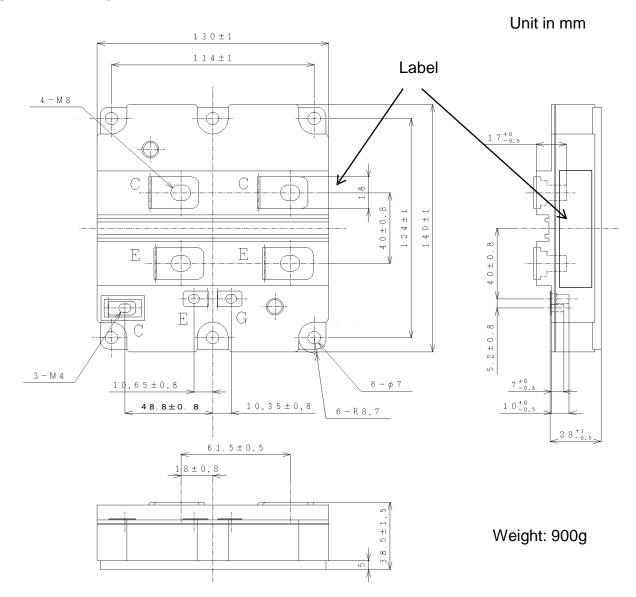
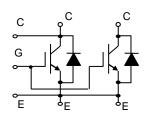


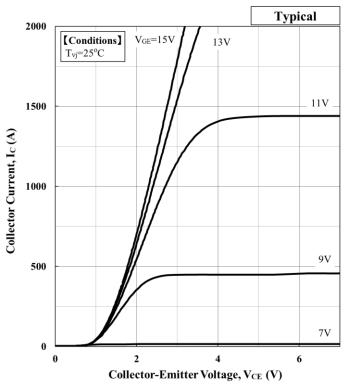
Fig.3 Definition of switching loss

OUTLINE DRAWING



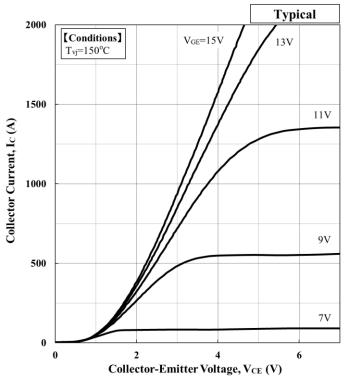
CIRCUIT DIAGRAM





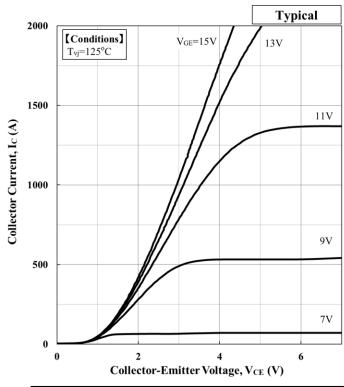
$V_{\text{CE}}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	V _{GE} [V]	a_1	a_0				
25	15	1.37E-10	-5.90E-07	1.71E-03	1.05E+00		

Collector Current vs. Collector Emitter Voltage



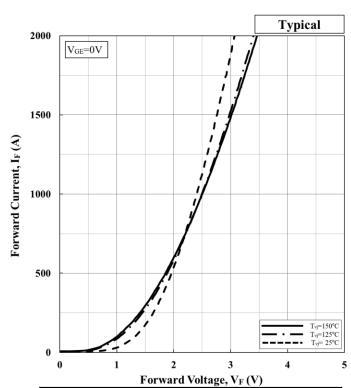
	$V_{\text{CE}}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C] $V_{GE}[V]$ a_3 a_2 a					a_1	a_0		
	150	15	2.25E-10	-9.45E-07	2.81E-03	1.03E+00		

Collector Current vs. Collector Emitter Voltage



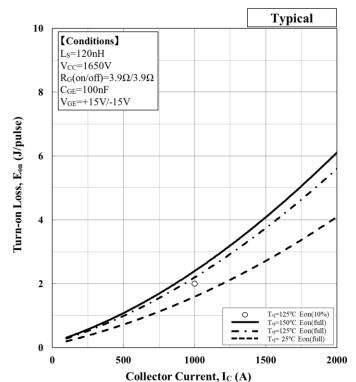
	$V_{\text{CE}}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C] $V_{GE}[V]$ a_3 a_2					a_1	a_0		
	125	15	2.32E-10	-9.48E-07	2.64E-03	1.03E+00		

Collector Current vs. Collector Emitter Voltage



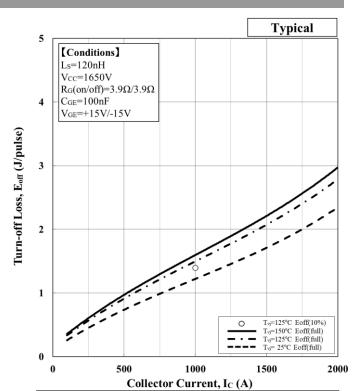
$V_{F}[V] = a_{3} \cdot I_{F} ^{3} + a_{2} \cdot I_{F} ^{2} + a_{1} \cdot I_{F} + a_{0}$								
Temp.[°C] a_3 a_2 a_1 a_0								
25	2.00E-10	-8.75E-07	1.90E-03	1.19E+00				
125	2.35E-10	-1.07E-06	2.47E-03	8.75E-01				
150	2.27E-10	-1.04E-06	2.52E-03	8.17E-01				

Forward Voltage of free-wheeling diode



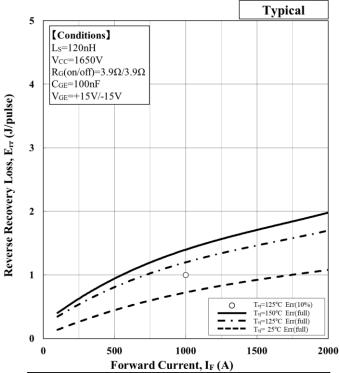
001100101 011110110,12 (11)								
$E\left[J\right] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$								
Temp.[°C]	a_3	a_2	a_1	a_0				
25	1.27E-11	4.60E-07	1.03E-03	9.08E-02				
125	-3.42E-11	7.79E-07	1.30E-03	1.50E-01				
150	-3 74F-11	8 50F-07	1 42F-03	1 64F-01				

Turn-on loss vs. Collector current



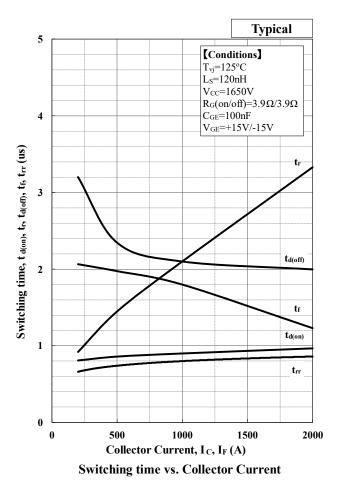
$E[J] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$								
Temp.[$^{\circ}$ C] a_3 a_2 a_1 a_0								
25	1.86E-10	-5.46E-07	1.46E-03	1.18E-01				
125	1.97E-10	-6.12E-07	1.74E-03	1.66E-01				
150	2.10E-10	-6.53E-07	1.86E-03	1.78E-01				

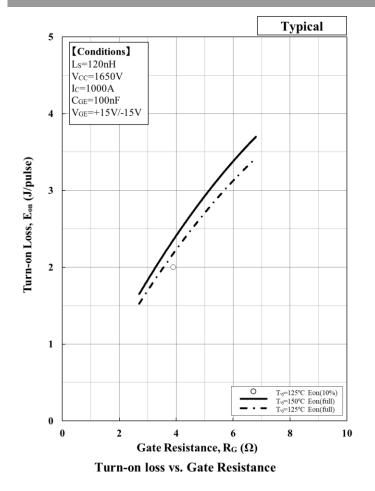
Turn-off loss vs. Collector current

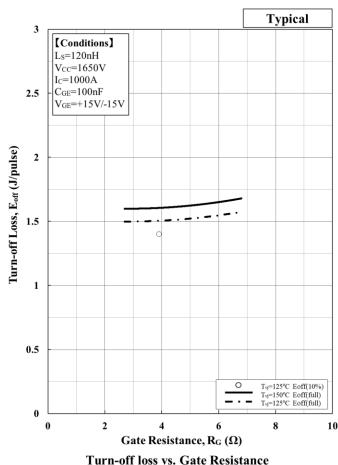


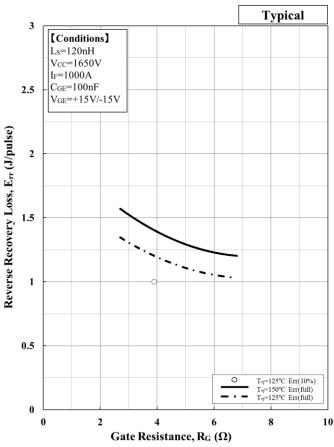
7 - ()								
$E[J] = a_3 \cdot I_F ^3 + a_2 \cdot I_F ^2 + a_1 \cdot I_F + a_0$								
Temp.[°C]	a_3	a_2	a_1	a_0				
25	5.36E-11	-3.23E-07	9.50E-04	4.42E-02				
125	1.23E-10	-6.17E-07	1.49E-03	2.01E-01				
150	1.43E-10	-7.20E-07	1.74E-03	2.34E-01				

Recovery loss vs. Forward current

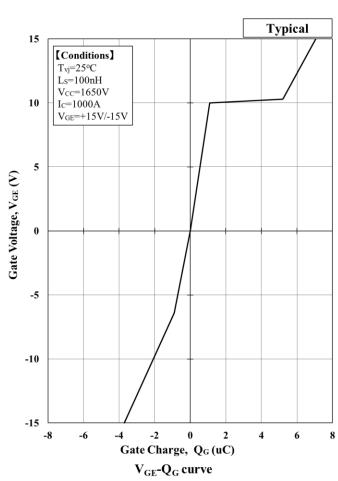


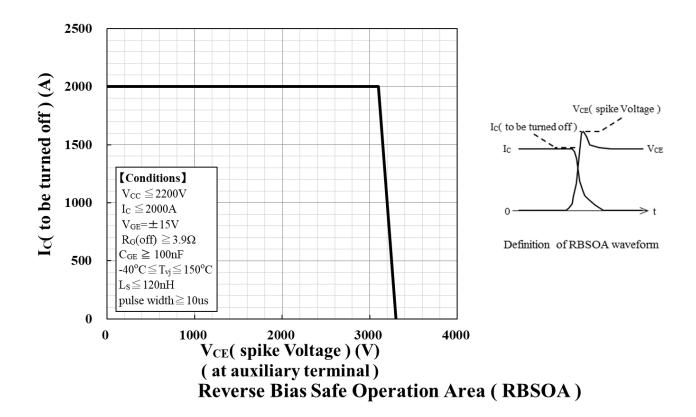


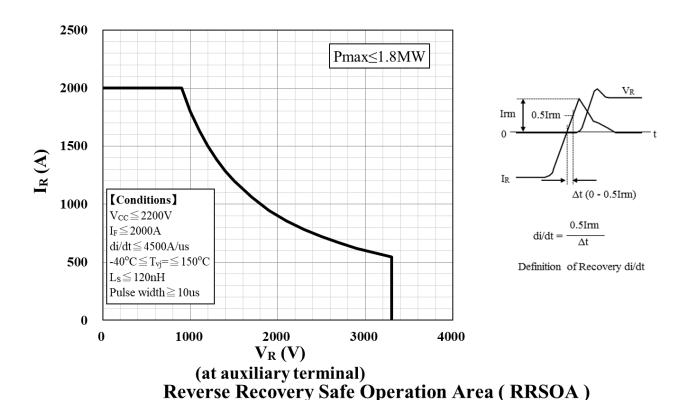


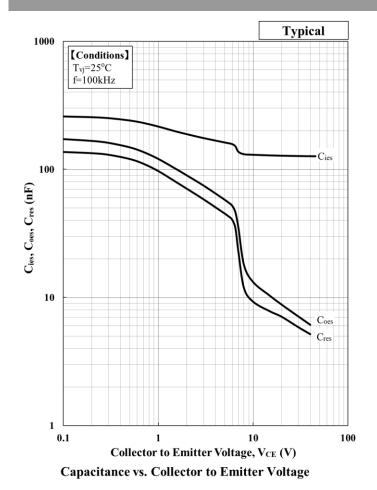


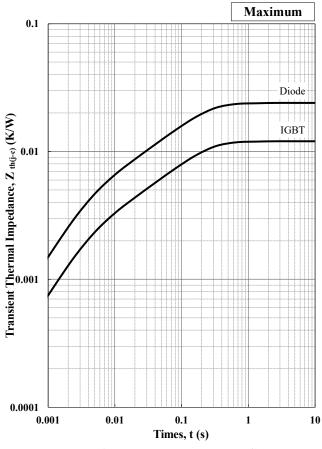
Reverse Recovery loss vs. Gate Resistance











Transient Thermal Impedance Curve

Foster model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	7.46E-03	2.17E-03	2.16E-03	2.21E-04
C th, IGBT [n]	2.14E+01	1.27E+01	1.88E+00	3.33E+00
R th, Diode [n]	1.48E-02	4.47E-03	4.24E-03	4.53E-04
C th, Diode [n]	1.07E+01	6.14E+00	9.54E-01	1.63E+00

Cauer model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	1.70E-03	1.93E-03	4.15E-03	4.22E-03
C th, IGBT [n]	1.04E+00	1.15E+00	8.01E+00	2.38E+01
R th, Diode [n]	3.35E-03	3.88E-03	8.33E-03	8.45E-03
C th, Diode [n]	5.21E-01	5.81E-01	3.92E+00	1.20E+01

Material declaration

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

Minebea POWER SEMICONDUCTORS

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