Silicon N-channel IGBT 4500V F version

FEATURES

* Soft switching behavior, low switching loss & low conduction loss :

Soft low-injection punch-through

Advanced Trench High conductivity IGBT.

- * Low driving power due to low input capacitance with trench MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High Current rate Package.
- * Low $R_{th(j-c)}$ & low stray inductance.
- * RoHS

ABSOLUTE MAXIMUM RATINGS (Tc=25°C)

Item		Symbol	Unit	MBN1500FH45F-H	
Collector Emitter Voltage		V _{CES}	V	4,500	
Gate Emitter Voltage		V _{GES}	V	±20	
Collector Current	DC	lc	A	1,500	
	1ms	I _{CRM}		3,000	
Forward Current	DC	IF	٨	1,500	
	1ms	I _{FRM}	— A	3,000	
Junction Temperature		T _{vj op}	°C	-50 ~ +150	
Storage Temperature		T _{stg}	°C	-50 ~ +150	
Isolation Voltage		V _{ISO}	V _{RMS}	10,200(AC 1 minute)	
Screw Torque	Terminals (M4/M8)	-	N.m	2/10 (1)	
	Mounting (M6)	-	IN•III	6 (2)	

Notes: (1) Recommended Value 1.8±0.2/9±1N·m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions	
Collector Emitter Cut-Off Current	ICES	mA	-	-	6	V _{CE} =4,500V, V _{GE} =0V, T _{vj} =25°C	
	ICES		-	-	180	V _{CE} =4,500V, V _{GE} =0V, T _{vj} =150°C	
Gate Emitter Leakage Current	I _{GES}	nA	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^{\circ}C$	
Collector Emitter Saturation Voltage	V _{CEsat}	V	-	4.35	5.0	I _C =1,500A, V _{GE} =15V, T _{vj} =150°C	
Gate Emitter Threshold Voltage	V _{GE(th)}	V	6.0	6.5	7.0	V _{CE} =10V, I _C =1,500mA, T _{vj} =25°C	
Input Capacitance	Cies	nF	-	83	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, T _{vj} =25°C	
Internal Gate Resistance	R _{G(int)}	Ω	-	2.6	-	V _{CE} =10V, V _{GE} =0V, f=100kHz, T _{vj} =25°C	
Turn On Delay Time	t _{d(on)}		-	0.5	-	V _{CC} =2,800V, I _C =1,500A	
Rise Time	tr		-	0.3	-	L _S =165nH	
Turn Off Delay Time	t _{d(off)}	μs	μ S -	2.6	-	$R_{G}(\text{on/off})=3.3/3.3\Omega$ (3)	
Fall Time	t _f		-	0.7	-	$V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$	
Forward Voltage Drop	VF	V	-	2.8	3.2	I _F =1,500A, V _{GE} =0V, T _{vj} =150°C	
Reverse Recovery Time	t _{rr}	μs	-	1.3	-	V _{CC} =2,800V, I _F =1,500A, L _S =165nH	
						T _{vj} =150°C	
Turn On Loss	Eon	J/P	-	5.1	-	V _{CC} =2,800V, I _C =1,500A, L _S =165nH	
Turn Off Loss	E _{off}	J/P	-	5.0	-	$R_{G}(on/off)=3.3/3.3\Omega$ (3)	
Reverse Recovery Loss	Err	J/P	-	5.6	-	V _{GE} =±15V, T _{vj} =150°C	
Short Circuit Pulse Width	+	μS	10	-	-	V _{CC} =3,000V, Ls=165nH	
	t _{sc}					$R_{G}(\text{on/off})=3.3/33\Omega, V_{GE}=\pm 15V, T_{vj}=150^{\circ}C$	
Partial discharge extinction voltage	Ve	V _{RMS}	3,500	-	-	f=50Hz, Q _{PD} ≤10pC(acc. to IEC 61287)	
Stray inductance module	L _{SCE}	nH	-	10	-		
Thermal Impedance	R _{th(j-c)}	K/W	-	-	0.0085	Junction to case	
FWD	R _{th(j-c)}	r\/ v v	-	-	0.0115		
		K/W	_	0.005		Case to fin (λ grease = 1W/(m·K)	
	R _{th(c-f})	1.7.9.9	_	0.000	_	heat-sink flatness ≤ 50μm)	

Notes: (3) R_G value is a test condition value for evaluation, not recommended value.

Please, determine the suitable R_G value by measuring switching behaviors.

* Please contact our representatives at order.

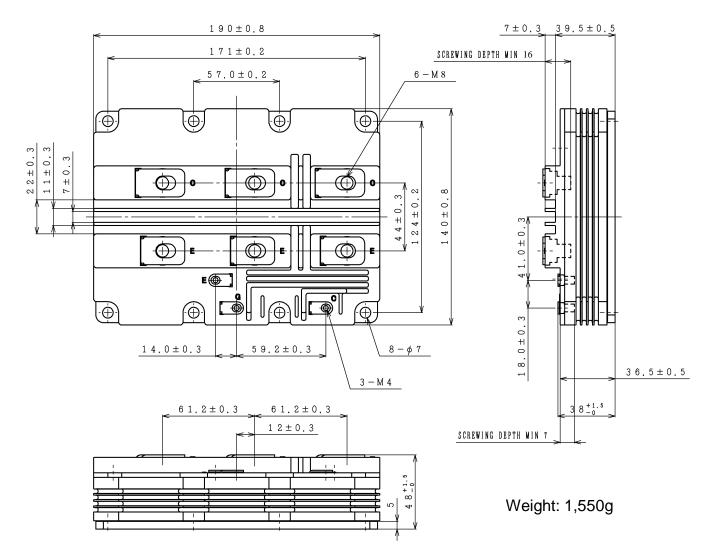
* For improvement, specifications are subject to change without notice.

* For actual application, please confirm this spec sheet is the newest revision.

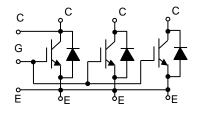
* ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

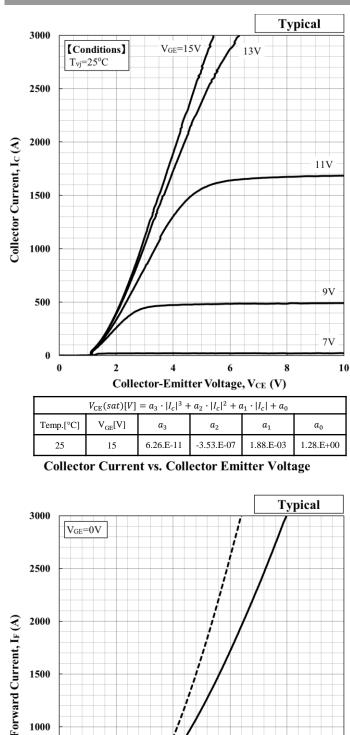
OUTLINE DRAWING

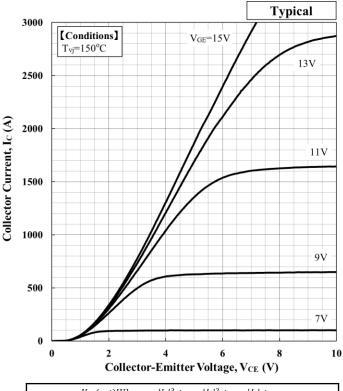
Unit in mm



CIRCUIT DIAGRAM

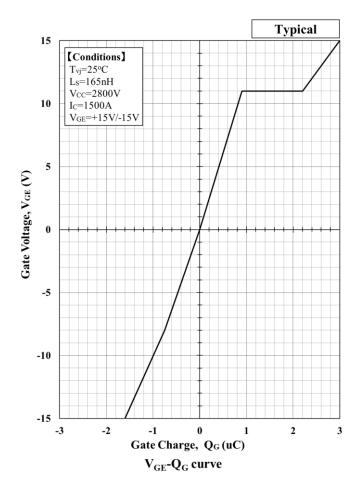






$V_{\rm CE}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	$V_{GE}[V]$	<i>a</i> ₃	<i>a</i> ₂	<i>a</i> ₁	a_0		
25	15	9.63.E-11	-5.27.E-07	2.74.E-03	1.13.E+00		

Collector Current vs. Collector Emitter Voltage



-4.69E-07 Forward Voltage of free-wheeling diode

a₂

-3.40E-07

Forward Voltage, V_F (V) $V_{\rm F}[V] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$

2

3

a₁

1.29E-03

1.84E-03

 $\begin{array}{l} T_{vj} = 150^{\circ}C \\ T_{vj} = 25^{\circ}C \end{array}$

 a_0

1.08E+00

8.94E-01

5

500

0

Temp.[°C]

25

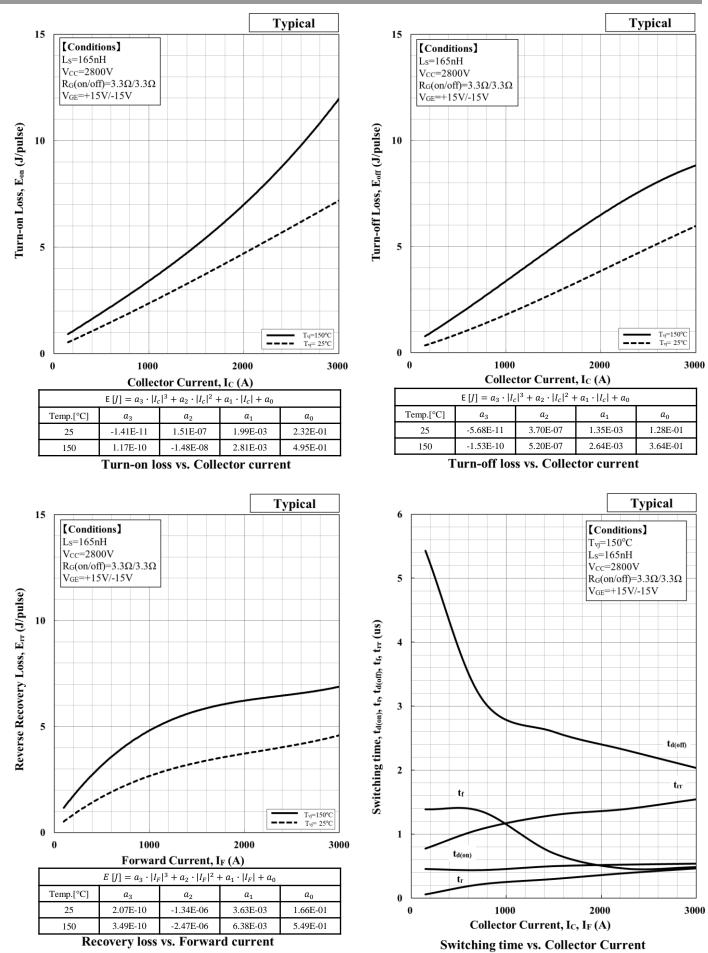
150

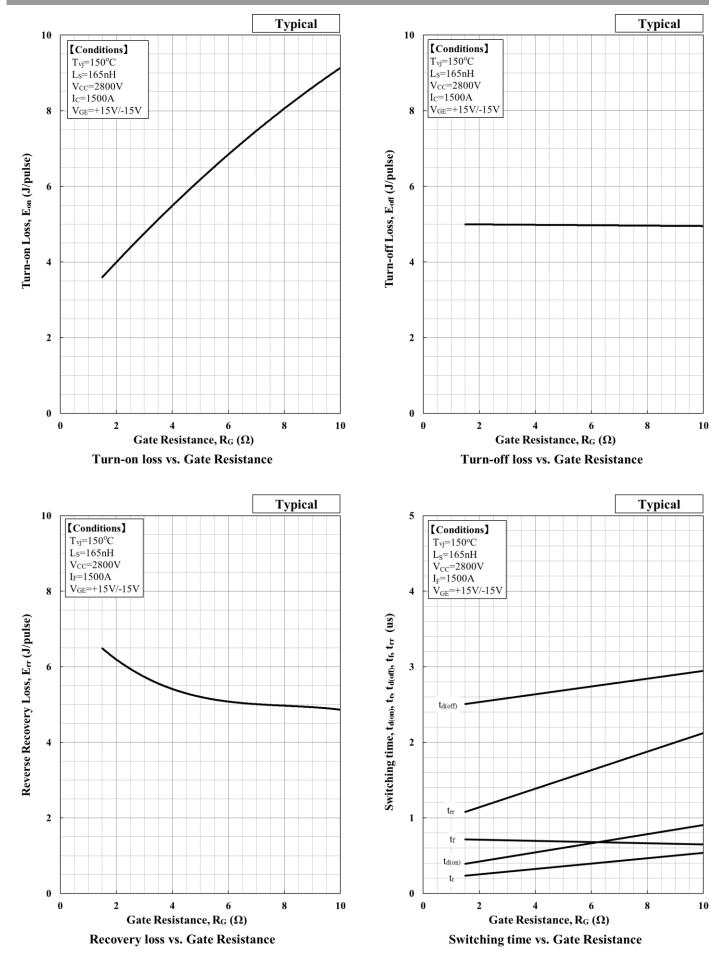
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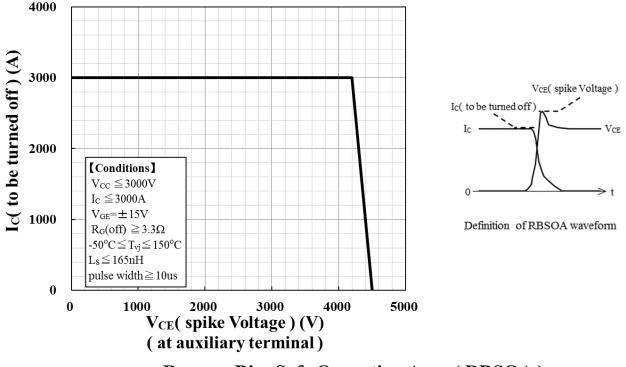
 a_3

4.92E-11

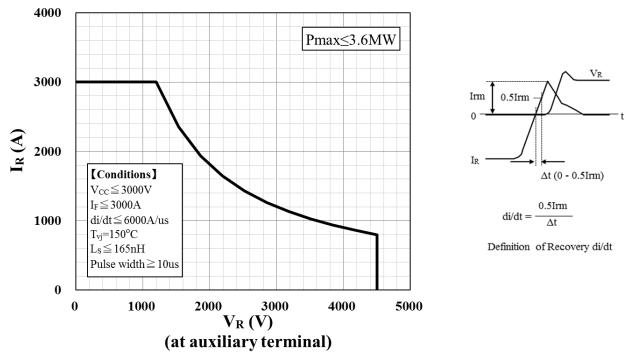
6.80E-11



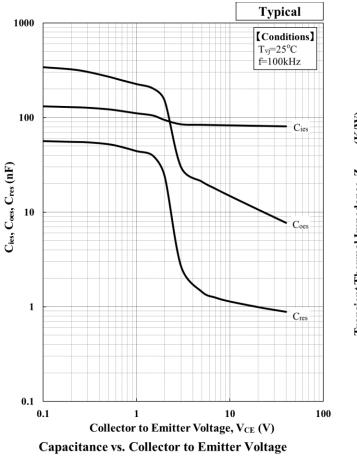


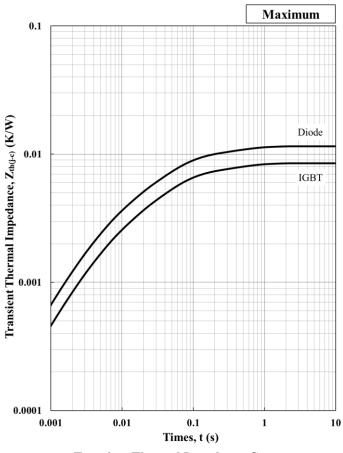






Reverse Recovery Safe Operation Area (RRSOA)





Transient Thermal Impedance Curve

Foster model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	1.76E-03	4.75E-03	1.63E-03	3.62E-04	[K/W]
C th, IGBT [n]	2.26E+02	9.79E+00	4.18E+00	6.17E+00	[J/K]
R th, Diode [n]	2.34E-03	6.25E-03	2.33E-03	5.78E-04	[K/W]
C th, Diode [n]	1.70E+02	7.43E+00	2.93E+00	3.86E+00	[J/K]

Cauer model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	2.35E-03	1.74E-03	3.11E-03	1.30E-03	[K/W]
C th, IGBT [n]	1.97E+00	3.35E+00	8.89E+00	2.90E+02	[J/K]
R th, Diode [n]	3.25E-03	2.39E-03	4.14E-03	1.73E-03	[K/W]
C th, Diode [n]	1.35E+00	2.49E+00	6.92E+00	2.18E+02	[J/K]

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