

MBN1500E33E2

Silicon N-channel IGBT 3300V E2 version

FEATURES

- * Soft switching behavior & low conduction loss:
Soft low-injection punch-through High conductivity IGBT.
- * Low driving power due to low input capacitance MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High thermal fatigue durability:
($\Delta T_c=70K$, $N>30,000$ cycles)
AlSiC base-plate/AlN substrate

ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$)

Item	Symbol	Unit	MBN1500E33E2
Collector Emitter Voltage	V_{CES}	V	3,300
Gate Emitter Voltage	V_{GES}	V	± 20
Collector Current	DC	A	1,500
	1ms		3,000
Forward Current	DC	A	1,500
	1ms		3,000
Operating Junction Temperature	$T_{vj,op}$	$^\circ\text{C}$	-40 ~ +150
Storage Temperature	T_{stg}	$^\circ\text{C}$	-50 ~ +125
Isolation Voltage	V_{ISO}	V_{RMS}	6,000(AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	2/15 (1)
	Mounting (M6)	-	6 (2)

Notes: (1) Recommended Value $1.8\pm 0.2/15^{+0}_{-3}$ N·m

(2) Recommended Value 5.5 ± 0.5 N·m

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I_{CES}	mA	-	-	12	$V_{CE}=3,300V, V_{GE}=0V, T_{vj}=25^\circ\text{C}$
			-	20	60	$V_{CE}=3,300V, V_{GE}=0V, T_{vj}=125^\circ\text{C}$
Gate Emitter Leakage Current	I_{GES}	nA	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^\circ\text{C}$
Collector Emitter Saturation Voltage	V_{CEsat}	V	2.5	2.95	3.5	$I_C=1,500A, V_{GE}=15V, T_{vj}=125^\circ\text{C}$
			-	3.1	-	$I_C=1,500A, V_{GE}=15V, T_{vj}=150^\circ\text{C}$
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	5.5	6.3	7.5	$V_{CE}=10V, I_C=1,500mA, T_{vj}=25^\circ\text{C}$
Input Capacitance	C_{ies}	nF	-	195	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^\circ\text{C}$
Internal Gate Resistance	$R_{G(int)}$	Ω	-	1.0	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^\circ\text{C}$
Turn On Delay Time	$t_{d(on)}$	μs	-	1.0	-	$V_{CC}=1,650V, I_C=1,500A$
Rise Time	t_r		1.6	2.0	2.6	$L_S=100nH$
Turn Off Delay Time	$t_{d(off)}$		-	2.7	-	$R_G=2.7\Omega/2.7\Omega, C_{GE}=330nF$ (3)
Fall Time	t_f		0.9	1.7	2.6	$V_{GE}=\pm 15V, T_{vj}=125^\circ\text{C}$
Forward Voltage Drop	V_F	V	2.2	2.6	3.0	$I_F=1,500A, V_{GE}=0V, T_{vj}=125^\circ\text{C}$
			-	2.6	-	$I_F=1,500A, V_{GE}=0V, T_{vj}=150^\circ\text{C}$
Reverse Recovery Time	t_{rr}	μs	0.2	0.8	1.2	$V_{CC}=1,650V, I_F=1,500A, L_S=100nH$ $T_{vj}=125^\circ\text{C}, R_G=2.7\Omega/2.7\Omega, C_{GE}=330nF$ (3)
Short Circuit Pulse Width	t_{sc}	μs	10	-	-	$V_{CC}=2,000V, L_S=80nH$ $R_G(on/off)=2.7/27\Omega, V_{GE}=\pm 15V, T_{vj}=125^\circ\text{C}$
Turn On Loss	$E_{on(10\%)}$	J/P	-	2.9	3.6	$T_{vj}=125^\circ\text{C}$
	$E_{on(full)}$		-	3.2	-	$T_{vj}=150^\circ\text{C}$
Turn Off Loss	$E_{off(10\%)}$	J/P	-	2.2	2.6	$T_{vj}=125^\circ\text{C}$
	$E_{off(full)}$		-	2.4	-	$T_{vj}=150^\circ\text{C}$
Reverse Recovery Loss	$E_{rr(10\%)}$	J/P	-	1.4	1.9	$T_{vj}=125^\circ\text{C}$
	$E_{rr(full)}$		-	1.7	-	$T_{vj}=150^\circ\text{C}$

Notes: (3) R_G and C_{GE} value are a test condition value for evaluation, not recommended value.

Please, determine the suitable R_G value by measuring switching behaviors.

- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.

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THERMAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Thermal Impedance	IGBT	$R_{th(j-c)}$	-	-	0.0078	Junction to case
	FWD	$R_{th(f-c)}$	-	-	0.0156	
Contact Thermal Impedance	$R_{th(c-f)}$	K/W	-	0.005	-	Case to fin

MODULE MECHANICAL CHARACTERISTICS

Item	Unit	Characteristics	Conditions	
Weight	g	1,300		
Stray inductance in module	LS(CM-EM)	12	Collector-main to Emitter-main	
	LS(ES-EM)	49	Emitter-sense to Emitter-main	
	LS(CM-CS)	56	Collector-main to Collector sense	
Terminal Resistance	$R_{Terminal}$	mΩ	0.09	Collector-main to Emitter-main
Comparative Tracking Index (CTI)	-	600		
Module base plate Material	-	Al-SiC		
Baseplate Thickness	mm	5		
Insulation plate Material	-	AlN		
Terminal Surface treatment	-	Ni plating		
Case Material	-	Poly-Phenylene Sulfide		
Fire and Smoke Category	-	I2 / F3	NFF 16-102	

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DEFINITION OF TEST CIRCUIT

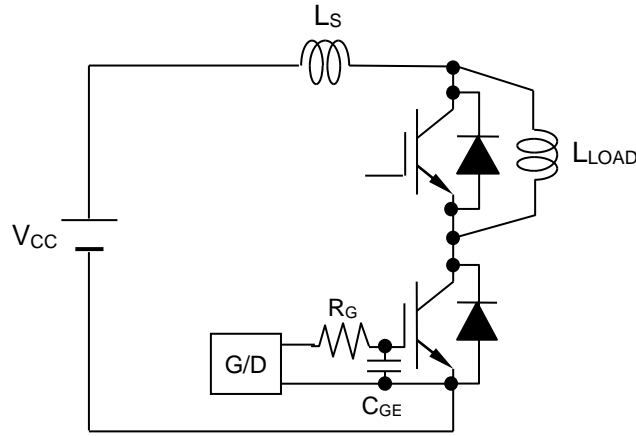


Fig.1 Switching test circuit

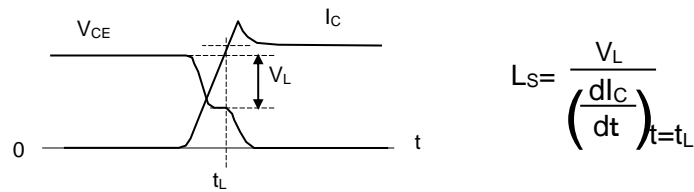


Fig.2 Definition of stray inductance

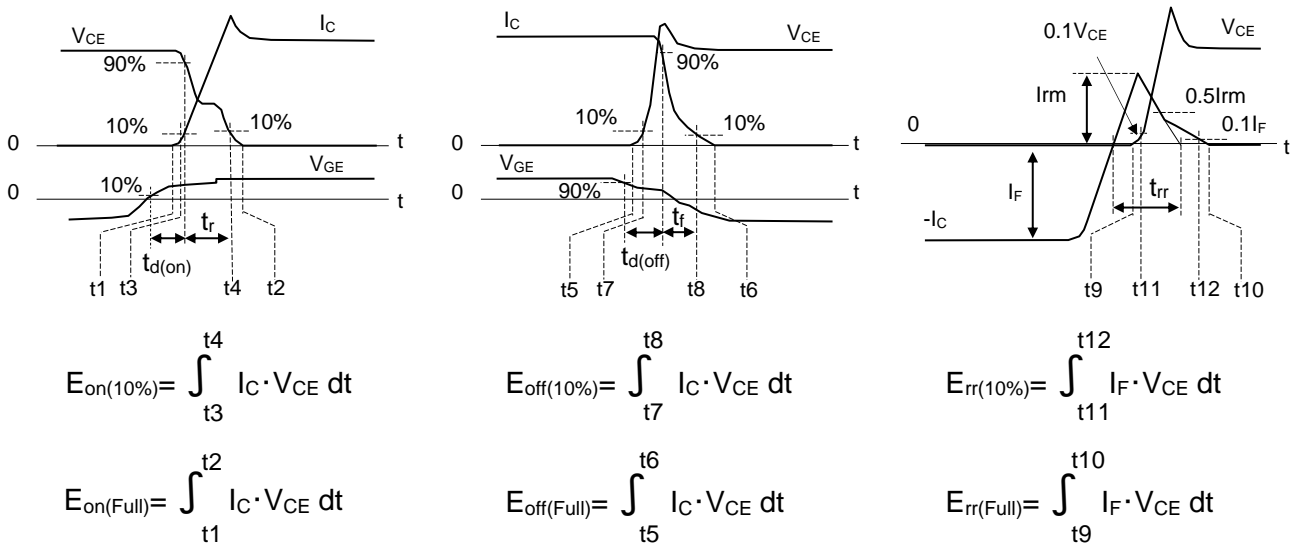
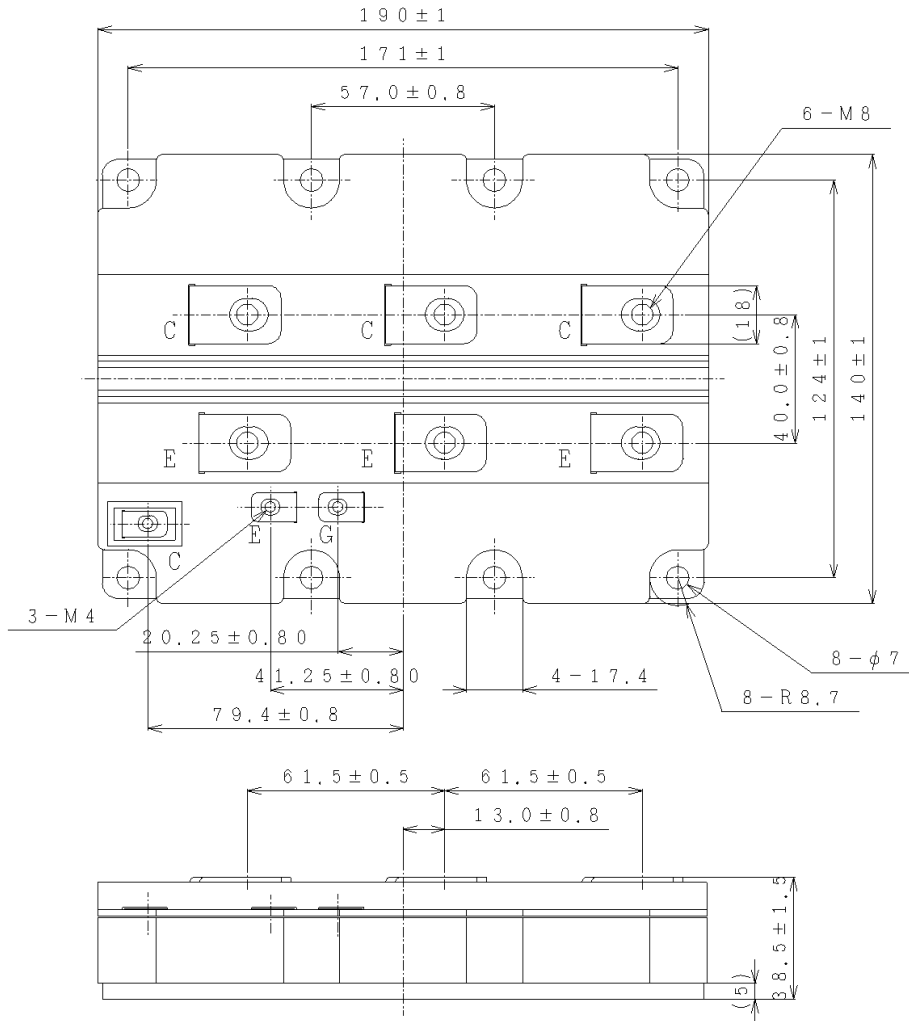


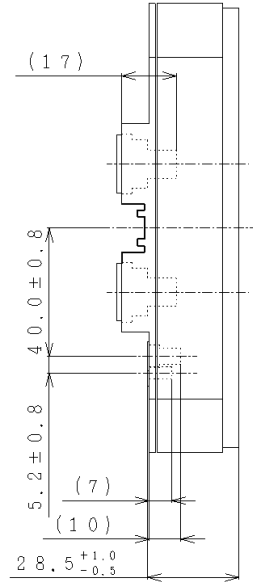
Fig.3 Definition of switching loss

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OUTLINE DRAWING

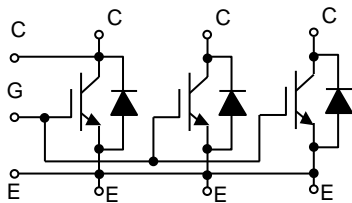


Unit in mm

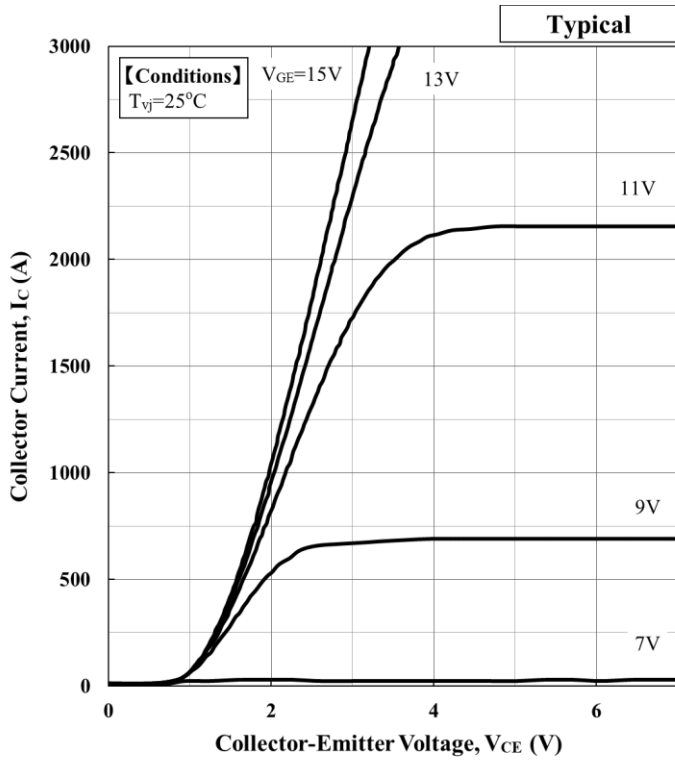


Weight: 1,300g

CIRCUIT DIAGRAM



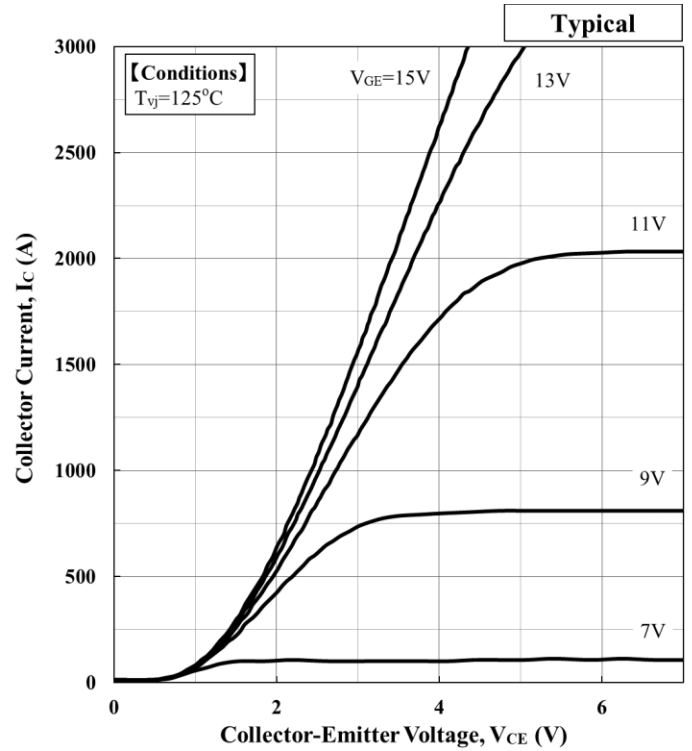
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$$V_{CE}(sat)[V] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	V _{GE} [V]	a ₃	a ₂	a ₁	a ₀
25	15	3.89E-11	-2.58E-07	1.15E-03	1.04E+00

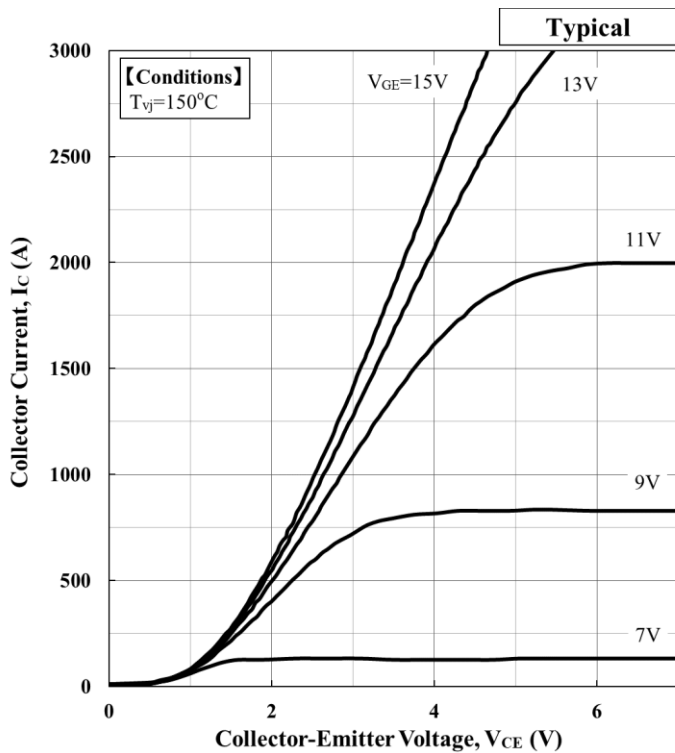
Collector Current vs. Collector Emittor Voltage



$$V_{CE}(sat)[V] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	V _{GE} [V]	a ₃	a ₂	a ₁	a ₀
125	15	5.71E-11	-3.73E-07	1.71E-03	1.03E+00

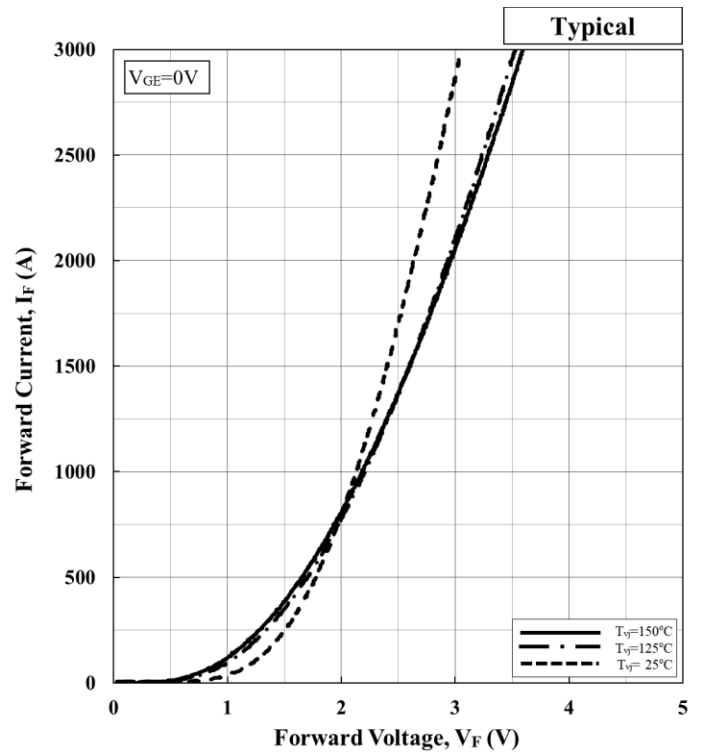
Collector Current vs. Collector Emittor Voltage



$$V_{CE}(sat)[V] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	V _{GE} [V]	a ₃	a ₂	a ₁	a ₀
150	15	6.33E-11	-4.08E-07	1.87E-03	1.01E+00

Collector Current vs. Collector Emittor Voltage

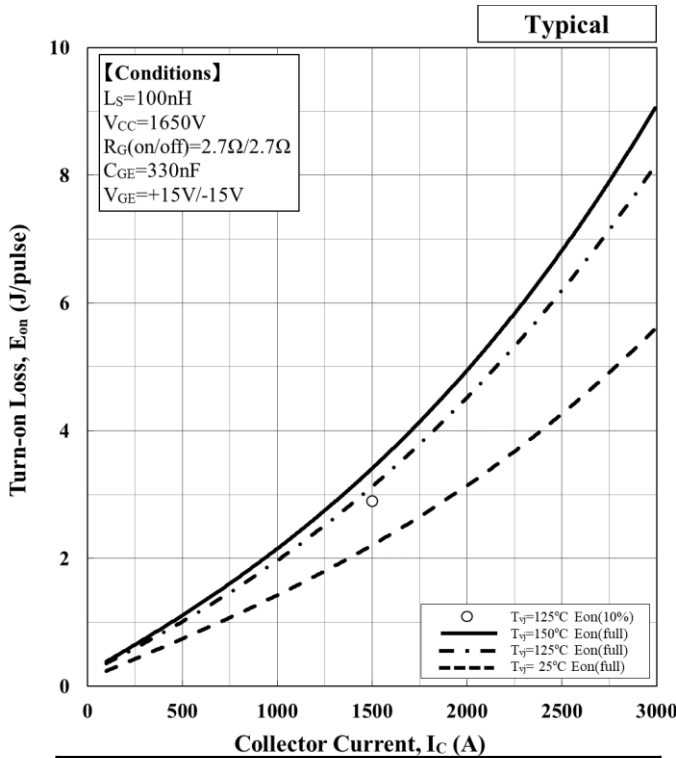


$$V_F[V] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$$

Temp.[°C]	a ₃	a ₂	a ₁	a ₀
25	4.65E-11	-3.30E-07	1.17E-03	1.24E+00
125	5.56E-11	-4.09E-07	1.57E-03	9.85E-01
150	5.80E-11	-4.20E-07	1.64E-03	9.10E-01

Forward Voltage of free-wheeling diode

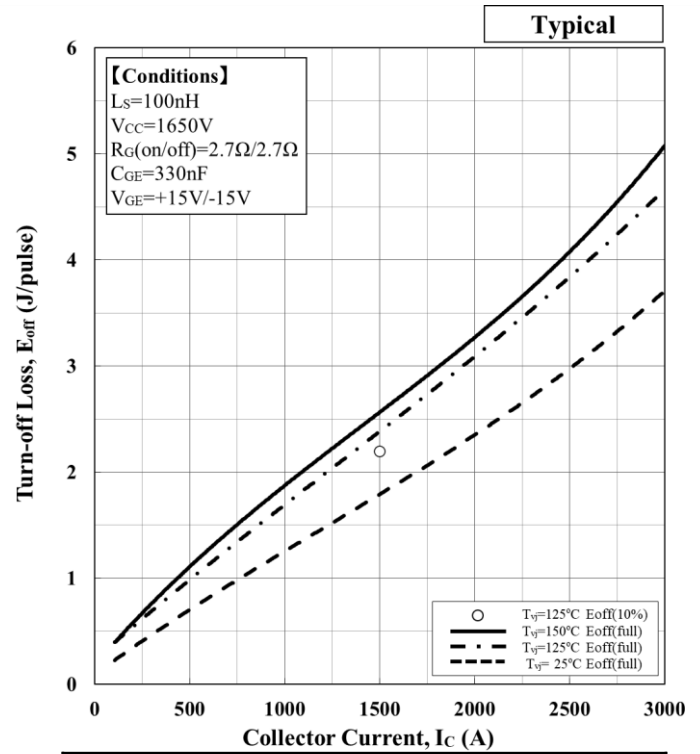
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$$E [J] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	a_3	a_2	a_1	a_0
25	5.92E-11	2.73E-08	1.22E-03	1.21E-01
125	5.34E-11	2.38E-07	1.46E-03	2.15E-01
150	8.41E-11	1.76E-07	1.67E-03	2.21E-01

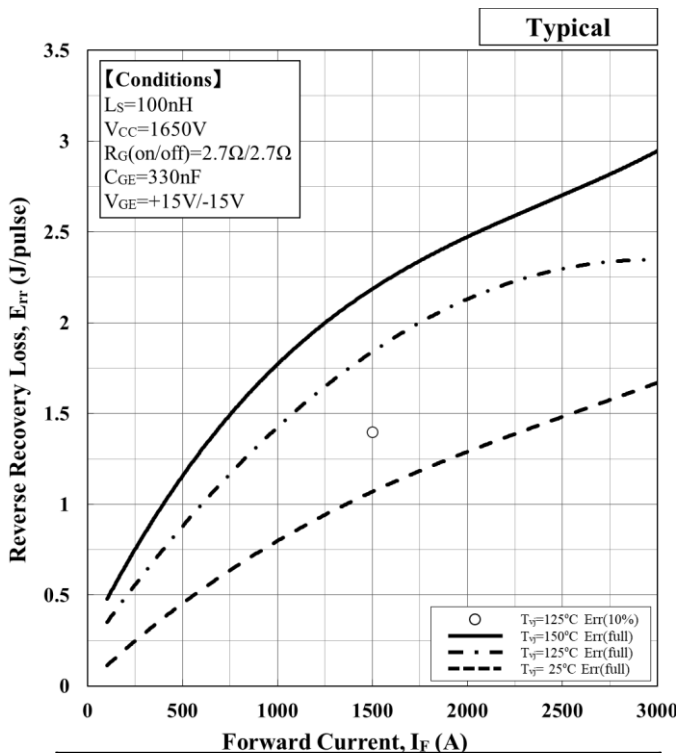
Turn-on loss vs. Collector current



$$E [J] = a_3 \cdot |I_c|^3 + a_2 \cdot |I_c|^2 + a_1 \cdot |I_c| + a_0$$

Temp.[°C]	a_3	a_2	a_1	a_0
25	5.45E-11	-1.96E-07	1.30E-03	9.35E-02
125	4.03E-11	-1.52E-07	1.57E-03	2.32E-01
150	1.18E-10	-5.01E-07	2.08E-03	1.81E-01

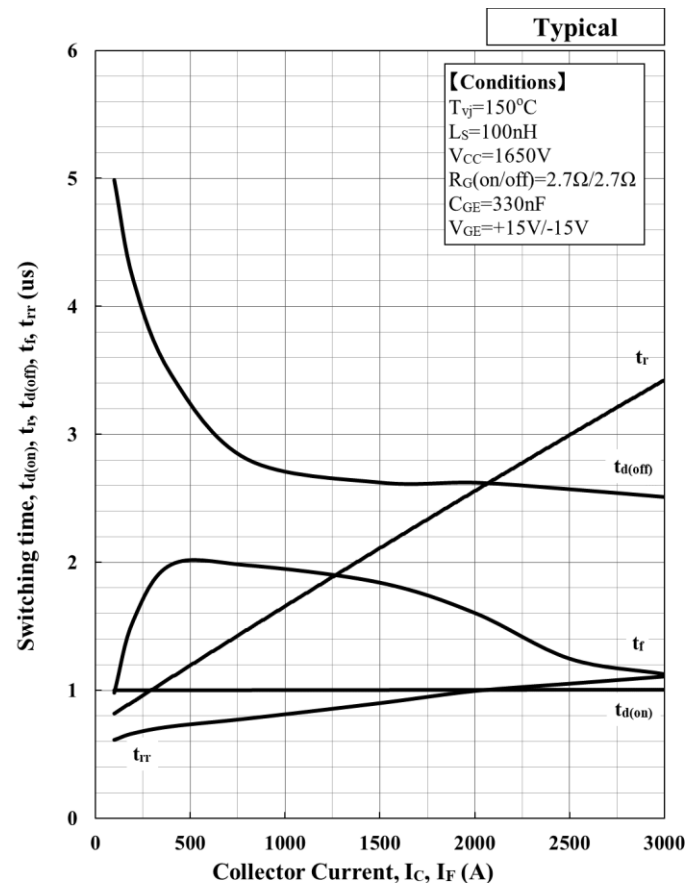
Turn-off loss vs. Collector current



$$E [J] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$$

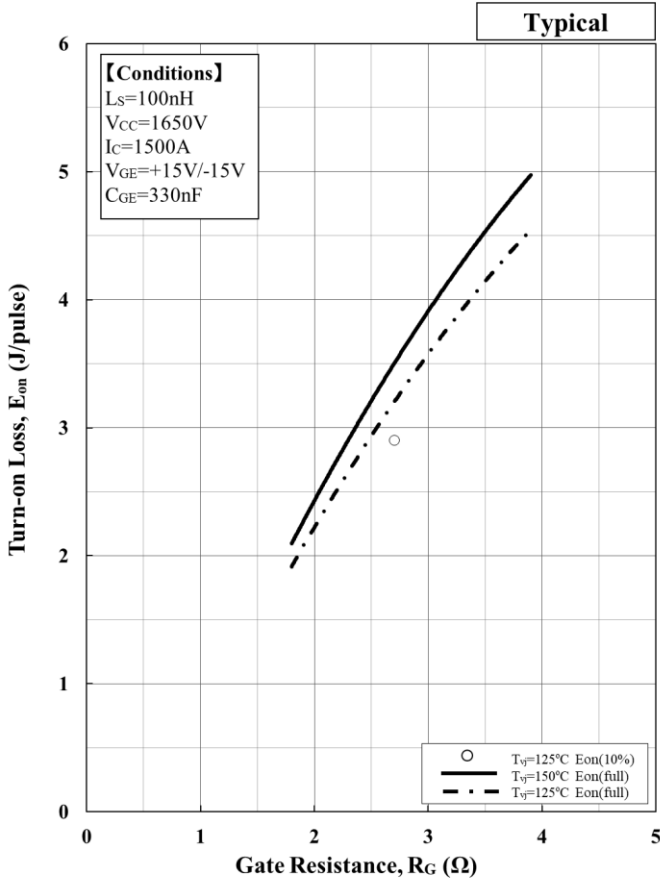
Temp.[°C]	a_3	a_2	a_1	a_0
25	3.07E-11	-2.40E-07	9.94E-04	1.33E-02
125	5.10E-12	-2.75E-07	1.50E-03	2.00E-01
150	9.49E-11	-6.84E-07	2.09E-03	2.71E-01

Recovery loss vs. Forward current

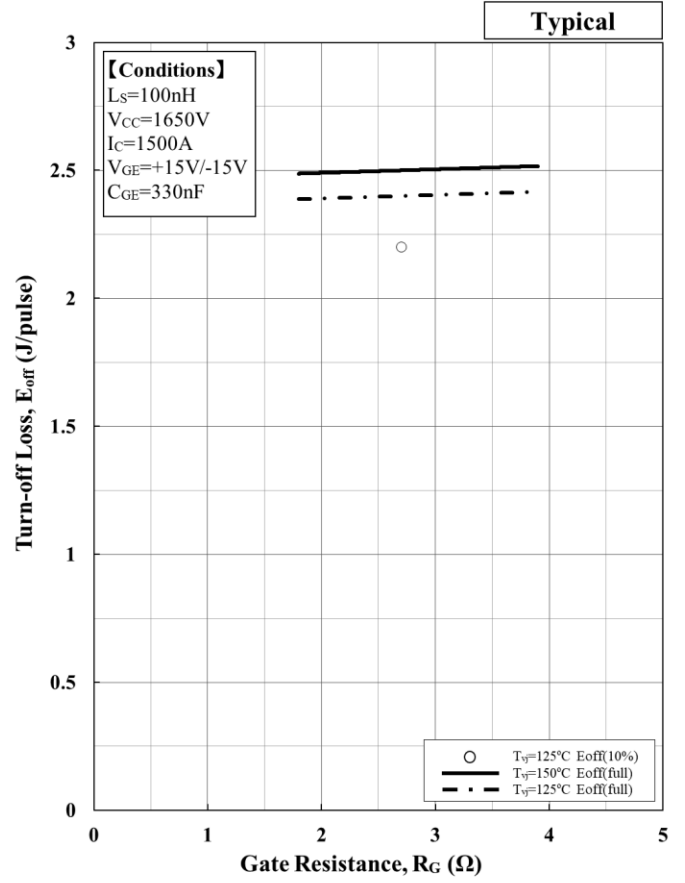


Switching time vs. Collector Current

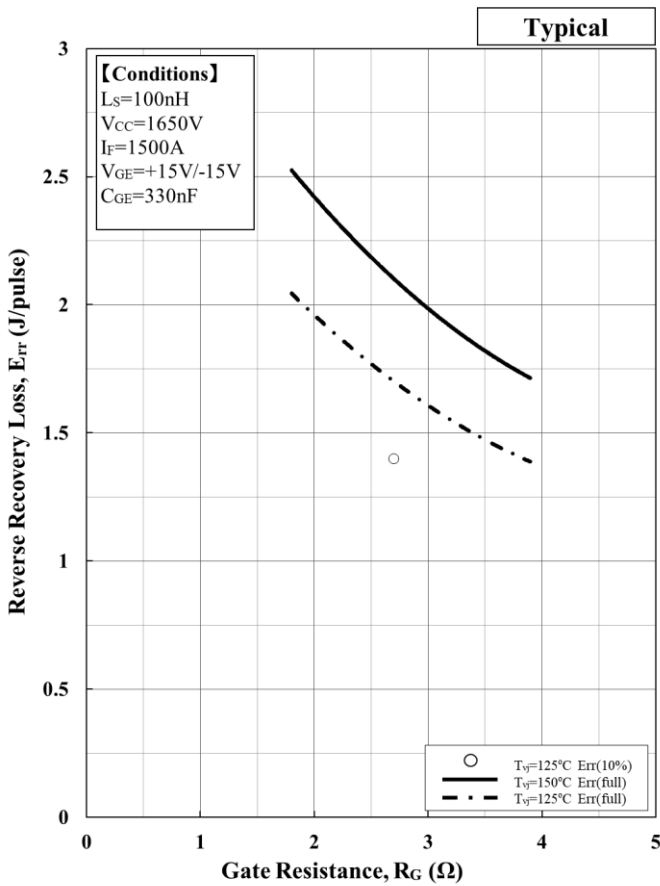
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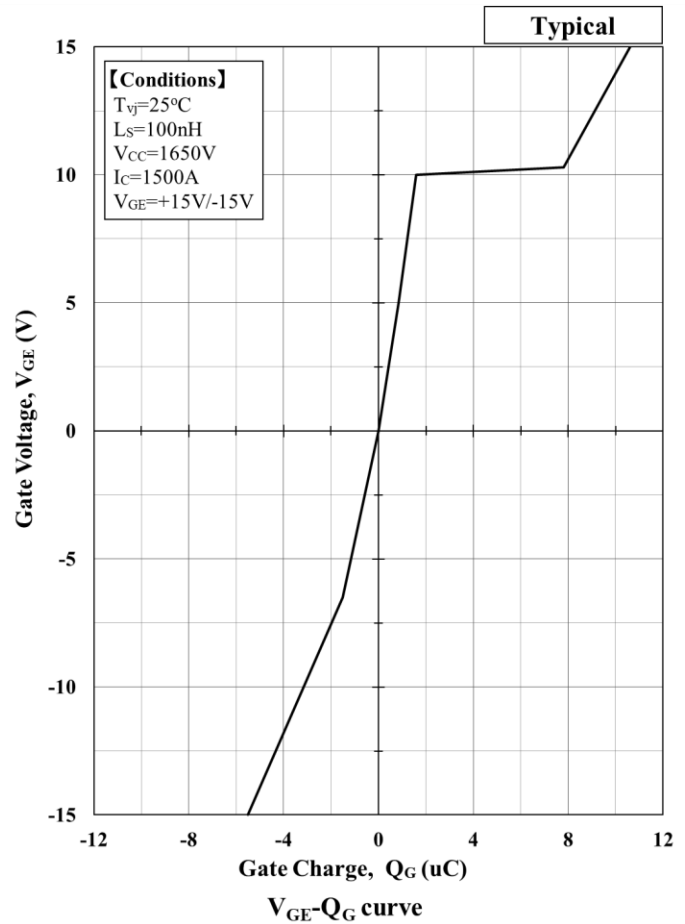
Turn-on loss vs. Gate Resistance



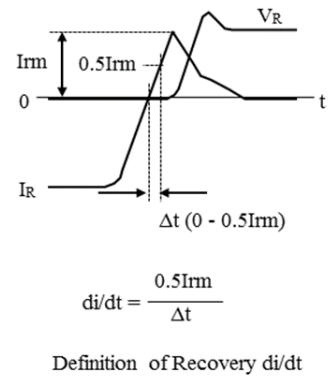
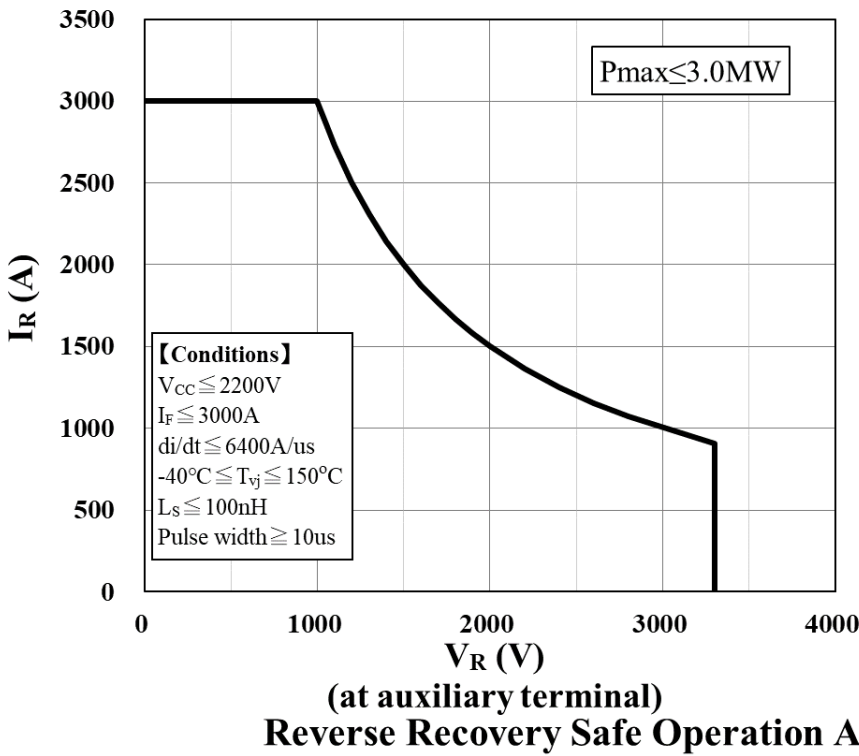
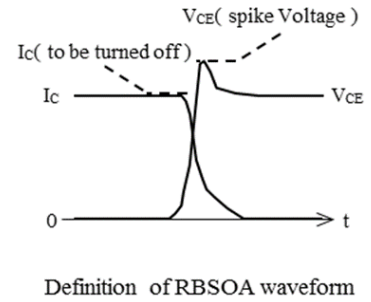
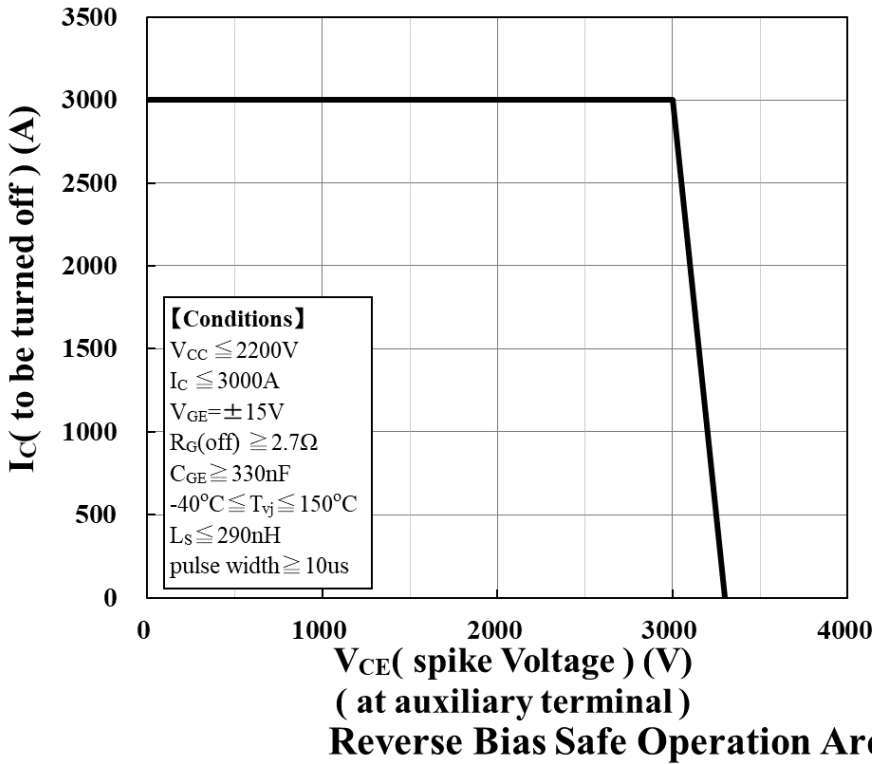
Turn-off loss vs. Gate Resistance



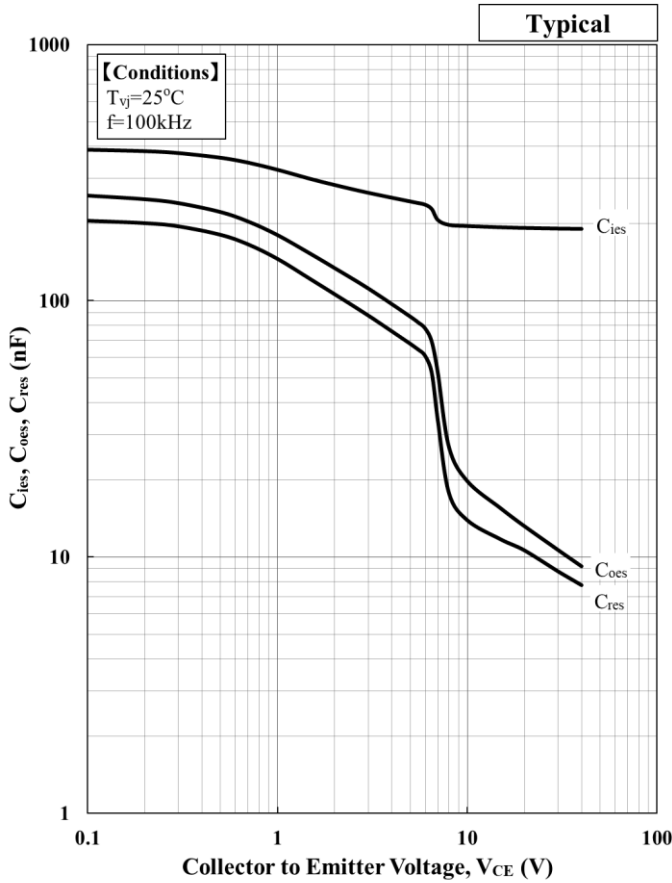
Reverse Recovery loss vs. Gate Resistance



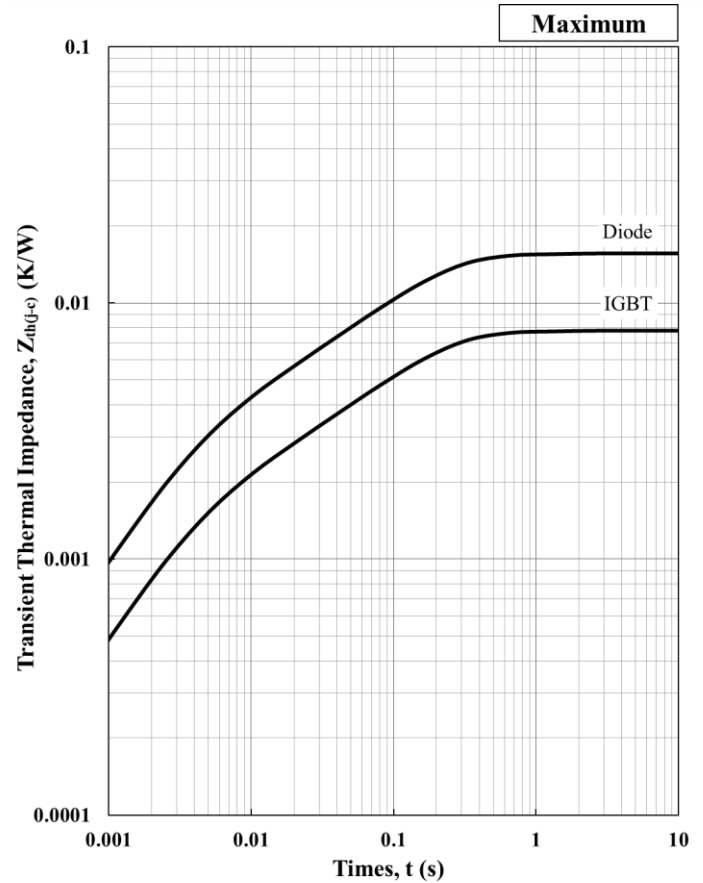
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Capacitance vs. Collector to Emitter Voltage



Transient Thermal Impedance Curve

Foster model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	4.86E-03	1.40E-03	1.40E-03	1.43E-04
C th, IGBT [n]	3.29E+01	1.95E+01	2.89E+00	5.14E+00
R th, Diode [n]	9.67E-03	2.90E-03	2.74E-03	2.93E-04
C th, Diode [n]	1.65E+01	9.47E+00	1.47E+00	2.51E+00

Cauer model lumped circuit constant

n	1	2	3	4
R th, IGBT [n]	1.10E-03	1.25E-03	2.70E-03	2.75E-03
C th, IGBT [n]	1.61E+00	1.77E+00	1.23E+01	3.64E+01
R th, Diode [n]	2.17E-03	2.52E-03	5.42E-03	5.50E-03
C th, Diode [n]	8.05E-01	8.96E-01	6.04E+00	1.84E+01

Material declaration

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

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Minebea POWER SEMICONDUCTORS

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2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
3. Semiconductor devices may sometimes break down by accidental or unexpected surge voltage, so please be careful about the safety design such as redundant design and malfunction prevention design which don't cause the damage expand even if they break down.
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5. A semi-processed article is done now using solder which contains lead inside the semiconductor devices. There is possibility of the regulation substance depend on the applied models, so please check before using.
6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
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8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).

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- For inquiries relating to the products, please contact nearest representatives that is located "Inquiry" portion on the top page of a home page.
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